

Raspberry for remote services

Abstract and Introduction

This technical report is about installing and using Raspberry Pi machines as little distributed servers especially for embedded process control applications. That means: With the applications in question we always control external devices. And we do that almost always via Pi's I/O ports. (Exceptions are MQTT and Modbus devices as well as VoIP telephones).

It was first published 2016 as a handbook for lab process control experimental set ups with Pi 2 and was continuously used and updated up to Pi4 on a wide spectrum of real control applications in 24/7 use. About May 2024 an updated (Pi5 etc.) yet shorter version [33] will be published.

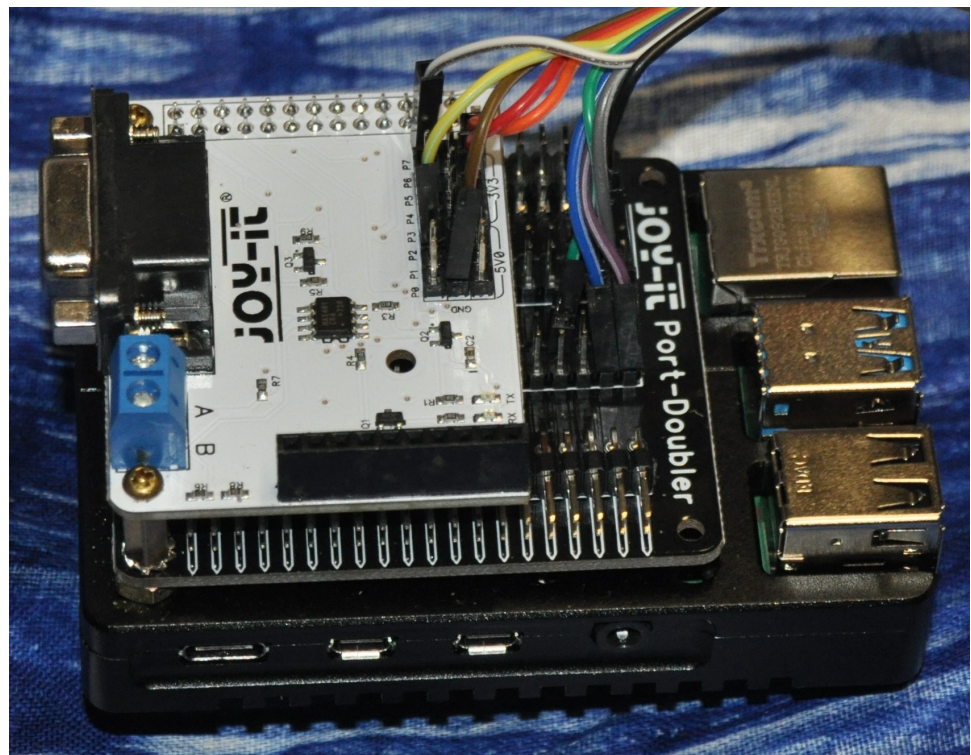


Fig. 1: Raspi with I/O

Target machines are Raspberry Pi 3s, Zeros, 4s (Figure 1), and very very seldom Pi 1s or 2s.

The Pi's operating system will be a Raspbian (now known as Raspberry Pi OS) lite, be it Jessie, Stretch or Buster. Raspbian is a Debian derivative as is Ubuntu. Raspbian, by itself, is no real time OS. Nevertheless, obeying some caveats, the lite variant may well be used in applications where max. latencies of 200 μ s are tolerable.

With careful C programming we can implement PLC like applications with 1 ms as fastest cycle based on Raspbian lite. Under some conditions I/O programs in Java ([32]) are feasible.

Motivation and goals

One might use a Raspberry 3 or 4 as a little low performance PC, nevertheless well outperforming last century's early PCs. Usually a Raspberry will be configured as a device for special purposes, often embedded. Then monitor, keyboard and mouse will hardly be present. Even tough maintenance jobs and very first start of a freshly installed OS can be made via (putty) remote access.

Following that route we will have a headless server or device that

- boots quickly and robustly at power on
- starts all its applications and/or services automatically
- runs 24/7
- fits, connects and re-connects robustly in its LAN and/or WLAN
- connects to process I/O via GPIO, SPI, RS232/445, private LAN, 1-wire or other
- provides administrative access remotely via ssl/ssh/putty
- provides access to files via ftp (FileZilla, WinSCP)
- And we have a decent comfortable cross platform working environment.
best on a Windows workstation using Eclipse and GCC tools.

Looking up those points we find us quite near the properties we must have in a “real” server; compare the Fujitsu Siemens RAID examples in [29] (notwithstanding the latter being a 1000 times heavier). Hence, some solutions and procedures may look astonishingly alike.

On the content

In **Part I** we describe the basic installation and commissioning with Raspberries.

Here we get the common knowledge, procedures and tools almost always needed when working with these (little) systems.

Part II deals with some use cases and real applications

Main points are using (process) IO and implementing automatically starting, unattended running applications respectively services. We look at hardware interfaces, Web HMI, GPIO libraries, timing, latencies, threading and cyclic tasks, using exemplary demo applications as well as the “real stuff”.

Using names

Names and addresses used here (and in [27..31]) are not fictitious. This helps bringing examples, really working and proven, of commands, files, outputs etc. – without errors introduced by obfuscating. Of course, you'll have to adapt IP addresses, names numbers etc. to your environment and needs, even when this will not always be explicitly mentioned.

For [References](#) and [Abbreviations](#) please see the Appendix (page 62)., Refer to [29] for some Linux and server basics, abbreviations and else, also used here.

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P A R T I

Basic installation

The OS images mostly used are

- NOOBS only for those who don't know what distribution to chose
- Raspbian "lite", i.e. no graphics, and (almost never) the not lite variant.

For servers and process control with real time requirements we strongly recommend the "lite" variant having no GUI nor graphical tools. Standard Linuxes call that a "server distribution".

Preparing the SD-card

Have a μ SD card of suitable size and speed ready. For a long running server application with logs and critical timing scrapping here is wrong.

Raspberry's OS and the file system live on a SD-card. Best use Win32DiskImager (figure 2) to put the prepared OS image to the card; prior formatting is not necessary.

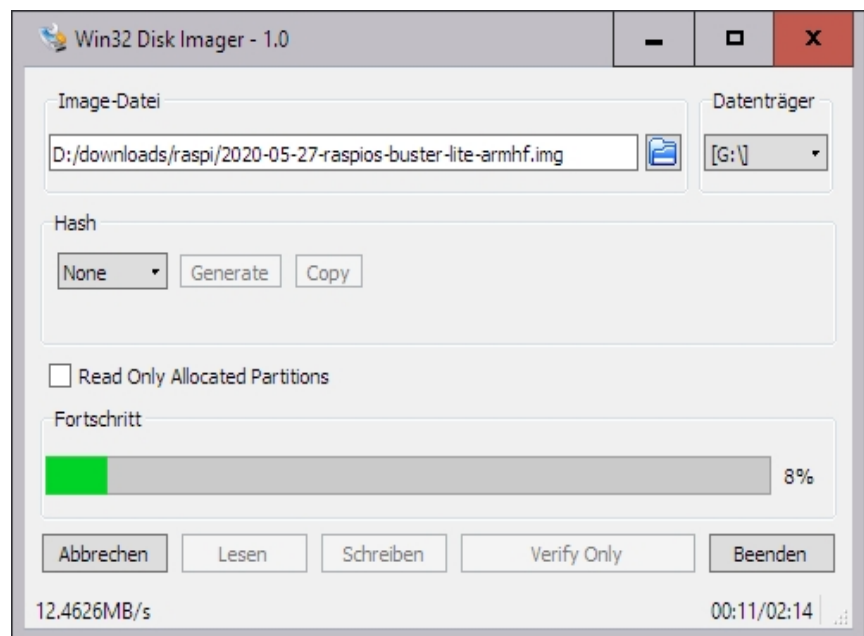


Fig. 2: Win32DiskImager .exe

Here you get a simple drive letter for the SD-card which you set on Win32DiskImager. When on Linux you'll have a hassle with mounts and un-mounts and use the (often unreliable) dd,.

Notes:

- a dd or DiskImager write will destroy all former content.
Be really sure to have the right device (mounted) on Linux
respectively the correct drive (letter) on Windows! (lest destroy your USB backup drive)
- Be patient. Burning takes some minutes (up to 2 min per GB)..
- If dd ends quickly, something went wrong (check the return code, if in doubt).

With Pi 4 and buster it might be possible to put the Pi's OS on an USB drive, if you intend to have one attached permanently.

Raspbian or Raspberry Pi OS

Debian based Raspbian, also called Raspberry Pi OS, is the most used Linux for Raspberry Pi's.

The distributions are commonly downloaded as .zip. Unzipping reveals one disk file (.img), like:

```
17.09.2020  1.845.493.760  2020-08-20-raspbian-buster-armhf-lite.img
27.05.2020  1.853.882.368  2020-05-27-raspbian-buster-lite-armhf.img
```

27.05.2020 3.829.399.552 2020-05-27-raspbian-buster-armhf.img

As of now, there's seldom a reason to use elder distributions, like (e.g.):

05.07.2017 1.725.629.563 2017-07-05-raspbian-jessie-lite.img

or "heavy" 4.285.005.824 2017-04-10-raspbian-jessie.img

13.11.2018 1.866.465.280 2018-11-13-raspbian-stretch-lite.img

20.06.2019 2.197.815.296 2019-06-20-raspbian-buster-lite.img

The .img is a pattern for the content of a µSD-card. "Burn" the image to the card and, optionally make some additional preparations. The Raspberry will boot from that card and, at the first boot, make the card its SSD.

Prepare headless access from start

If you want headless access after installation you must do this on the PC while having the [µ]SD-card mounted: Add a file named `ssh` (no extension, no content) on the SD card's root directory. This enables putty and ssh via LAN and avoids the keyboard layout horror; see note *) below. You need a DHCP server on your (W)LAN.

Prepare WLAN access from start

If you want this headless access from start via WLAN, too, try this: Put a file named `wpa_supplicant.conf` on the SD card's /boot/ or root / directory with this content:

```
country=DE
ctrl_interface=DIR=/var/run/wpa_supplicant GROUP=netdev
update_config=1
network={
    ssid=wlan-name
    psk=password or output of wpa_passphrase wlan-name pw
    key_mgmt=WPA-PSK
}
```

This will work since Stretch and for Buster. But n.b.: Read the hints and note on page 8.

With or without preparing headless access, un-mount the device, remove the [µ]SD-card from the workstation, put it in the target Raspberry.

Note on (semi) automating the burning: On Windows' mostly pure graphical tools the .img burning can't be (batch) automated. And if the tools have a command line there's still the "drive letter surprise" effect. On the other hand, Linux/Ubuntu is hardly better. Here you have the funny device and partition name surprise.

First commissioning

These are first steps to bring in a new OS with a freshly prepared [µ]SD card:

- take the not powered target Raspberry and put the card in
- x get the little machine to keyboard, LAN and HDMI monitor *)
- connect power (usually by USB 5V 2A)
- check your DNS server for your new Pi's IP address **)
 - Fritzbox at home e.g.: raspberrypi / DC:A6:32:50:72:6E connected to FRITZ!Box
- fire up putty or ssh command and connect to that IP **)

Note: **) if prepared for headless first start (recommend); *) keyboard etc. only if not prepared.

When all went well, you'll be prompted to log-in. Pre-defined user is "pi", password is "raspberry".

As most images are US with no real installer – keyboard layout and much else is utterly wrong. As a first consequence – when not on putty but on a "real" monitor and keyboard – the standard login will fail, while pi:rasberrz will succeed. If pi:rasberrz and pi:raspberry both fail, someone had the prudence to change the public first log-in of a public image.

Note: When making a “public” name:password never use any keys that are misplaced on US keyboards nor special ones on German or French. And never rely on the existence of the numeric pad.

Note *) again: All monitor and keyboard (layout) hassle can be avoided by **headless** installation.

After taking the login hurdle search the - (minus) or use the numeric keyboard to enter

```
sudo raspi-config
```

To use this tool, you need space, cursor keys and tab. Here the keyboard bug should not be a problem. Otherwise you're lost.

Now use `sudo raspi-config` to

- set your locale and the time zone
- the keyboard layout (not needed when only headless access is used)
- your country's WiFi settings(not needed when WLAN from start succeeded)
- and enable ssl (not needed with SSL from start)
- and enable vlc (when having chosen a non lite distro)
- x **do *not*** enable using all the rest of the µSD as disk (NOOBS may do that without order)
- change the (host) name (*this may not really work with raspi-config*)
- enable serial interface, but not as console (*if you wish to use it for devices*)

With working (W)LAN and in a network with a DHCP server you should see the Raspberry's usable IP address(es) by: `ifconfig`

If the machine met the same DHCP server before, the address(es) should stay the same. In your network, do order the DHCP server to give your controllers fixed stable addresses.

With ssl you may get a remote shell via ssh or putty. Using those on Windows instead of Ubuntu gives superb clipboard support that sooner or later will get essential for any remote work.

Tip 1: Do not chose "predictable" interface names if the question arises. You'll get the opposite.

Tip 2: Use your locale, encoding etc. but switch the output of the OS and the applications back to English, the Raspbian default language, by

```
export LC_ALL=C      # (put in ~/.bashrc to have it after reboot)
```

Setting the host name

When having more than one Raspberry in the [W]LAN you must give each one a unique name.

With Raspbian changing the host name must be done in three places:

by both editing `/etc/hostname` as well as `/etc/hosts` and by the `hostname` command:

```
sudo nano /etc/hostname
```

```
sudo hostname theNewName
```

```
sudo nano /etc/hosts          # correct / set 127.0.1.1 entry here
```

`raspi-config` usually forgets at least one step, leading to schizophrenic effects and error messages.

Enable WLAN – or disable it

The Pi3, Zero and 4 have WLAN on board (Pi1 has not). The build in WLAN's only disadvantage is the antenna being fixed to the board. Encasements with good protection and shielding may render it unusable. While LAN (with DHCP) is working out of the box, WLAN will require some settings. This is best done by command line, as well described in [53]. On the other hand, when having prepared headless WLAN access above, no further action may be needed.

A most useful command – even when not wanting to use WLAN – is listing visible WLANs by:

```
sudo iwlist wlan0 scan
```

If not sure if the WLAN is wlan0 use ifconfig. In our case it didn't see 5 GHz cells. The Pi3 (not 3+) supports 2.4 GHz only, even if the Broadcom chip should do both. From the iwlist scan output we chose a WLAN in acceptable quality (atMEVAnet in the example here) to connect to.

```
Cell 03 - Address: 44:94:FC:88:B0:40
          Frequency:2.452 GHz (Channel 9)
          Encryption key:on
          ESSID:"atMEVAnet"
          Mode:Master
          Extra:tsf=0000000000000000
          IE: IEEE 802.11i/WPA2 Version 1
              Group Cipher : CCMP
              Pairwise Ciphers (1) : CCMP
              Authentication Suites (1) : PSK
```

To make the Raspberry connect to one WLAN we have to append the name and encrypted *) password in a given syntax to the configuration file /etc/wpa_supplicant/wpa_supplicant.conf.

```
wpa_passphrase atMEVAnet
```

generates this appendix. You should obfuscate or delete the clear text password *) comment when appending the command output to /etc/wpa_supplicant/wpa_supplicant.conf:

```
country=DE
ctrl_interface=DIR=/var/run/wpa_supplicant GROUP=netdev
update_config=1
network={
    ssid="atMEVAnet"
    priority=1
    #psk="Kr.....wunkel"
    psk=e891c0f1aw3746098509ae92612cafe4d844b06cee0aab8a178488a88bb66712
}
```

So far this file is not device specific. It can be transferred to other Raspberries.

Note: This file, stupidly, wants tabs for the indentation. (Greetings from Unix' stone age!) Have no blanks around and have Unix-text-format (LF only) when preparing this file on a Windows workstation. Note *): It is best practise not to store the password psk="inCleartext" but psk=ebcreded1234 (the "" seems to be the syntactic difference). But we had cases a where a wpa_passphrased network={} simply didn't work (iPhone hotspot, e.g.).

It's also possible to add more than one network={...} entry, giving all an additional priority attribute with different values. Surprisingly, higher number mean higher priority [sic!]. If the configuration change has no effect, force it by:

```
sudo wpa_cli reconfigure # often has no effect; reboot will do
```

The time to disable Pi's WLAN (for security reasons e.g.) may come. y=1

To do so, change the end of /boot/config.txt to

```
# Enable audio (loads snd_bcm2835)
###dtparam=audio=on
enable_uart=1
### added
dtoverlay=pi3-disable-wifi
dtoverlay=pi3-disable-bt
```

and re-boot. Note 4: Here we decided to need no audio, too. Note 5: On Pi4 you may omit "pi3-".

Force IPv4

Even when all your LAN addresses are IPv4, you may notice wlan0 using IPv6 only, even when the access points live in the same LAN and the DHCP server distributes IPv4 to all. Add the line

```
net.ipv6.conf.all.disable_ipv6=1
```

to /etc/sysctl.conf

Now it's a good time to "Make consistent and comparable directory listings"; Appendix page 62. And, when at it, you may install the pigpio library (page 29) and libmodbus (page 40).

Update the Raspbian OS

Having the basic configurations done one should have a first thorough update:

```
sudo apt-get upgrade # <-\
sudo apt-get update   #      \_ loop until stable
sudo apt autoremove
sudo apt-get dist-upgrade
```

After installing and running productive (24/7 server) dist-upgrade should be handled with care.

Configuring putty

With ssl enabled, from a workstation or laptop in the same network connect to your Raspberry with ssh using putty(.exe). And making a good putty configuration is well worth the trouble. Think of nice and readable colours, a good font, window title etc.. When satisfied give the configuration a recognisable name, like e.g. rasp61. Best use the Raspberry's unique hostname. For multiple accesses (LAN and WLAN) you need extra names. To re-use the configuration command

```
putty -load rasp61 -l pi
```

Change the "-l pi" (meaning log-in as user pi) when wanting another user account to log-in or omit the "-l name" option to interactively entering the user. Optionally make an icon with the command.

If you want to copy, modify and reuse such configuration you may use the putty GUI by

a) load, b) modify c) save and/or open.

To save and transfer configurations across workstation borders, find them at

```
~/ .putty/sessions/
```

on Linux and on Windows in the registry at

```
[HKEY_CURRENT_USER\Software\SimonTatham\PuTTY\Sessions\<ses.name>]
```

Here is an example of a session's entry:

```
Windows Registry Editor Version 5.00
[HKEY_CURRENT_USER\Software\SimonTatham\PuTTY\Sessions\lite042]
"Present"=dword:00000001
"HostName"="192.168.89.42"
"LogFileName"="putty042.log"
```

As with the Linux files the reg text files may be transferred to your other cross development machines (without editing).

Tip: On Windows 10 you can connect the command shell (cmd.exe or power shell) with the Pi by

```
ssh pi@rasp61
```

The presentation usually outperforms putty. Ending the ssh session with logout returns to Windows.

Enabling file access

For administrative and programming work remote file access from workstations is essential. Accessing headless / GUI-less systems that way, you'll get decent editors and (on Windows) an operational clipboard handling worth the name.

Most Linux distributions for Raspberry, like Raspbian, will not have a FTP server. But almost all will have SSH. This offers remote file access by protocol sftp. SSHFS, FileZilla and WinSCP *) can handle that as clients. Hence, **no FTP server** installation on Raspbian will be needed.

Note *: Windows' ftp.exe can't – but on Windows you may have the excellent WinSCP.

SFTP – FileZilla

You really should have FileZilla on your Ubuntu and Windows workstations. (You may even have FileZilla on a graphical Raspbian.) On Linuxes install it by:

```
sudo apt-get update  ## *)
sudo apt-get upgrade
sudo apt-get install filezilla
```

On Windows download and install the newest Filezilla.

Now set up FileZilla as client: Open FileZilla, go to site manager, make a new site and name it accordingly, say “raspi61”.

In general setting do: host = 192.168.89.61; protocol: sftp; login: normal, pi, raspberry.
In “advanced” tick “bypass proxy”, when your workstation and Pies share a local network.

That's it. Connect should work.

For every other Pi just copy and modify IP and name. Enjoy

- disconnect and re-connect working perfectly
- comfortable view/modify integration with text editor set (best Editpad on Windows)
- see and explore all files from root (/) on, not just /home/pi/

The last point allows to comfortably view /etc/apt/sources.list (with Editpad on Windows) and copy its content without ado by clipboard.

Modifications in the editor on the (automatic) local copy will be possible, but in FileZilla's mirroring the changes back will fail for files with root:root permissions only. But we always can see, work on, copy, use clipboard etc. on multiple windows in a comfortable environment.

One workaround for modifying system files is opening a putty connection in parallel with FileZilla and transfer system files to be comfortably worked on to and (sudo) from a user working directory.

SFTP – WinSCP

WinSCP is one of the best FTP client programs – not for giving us the n + 21st graphical FTP but as a most powerful command line program and for its automated batch processing capabilities.

To install it, download the .zip file of a portable build, unpack it and move the three files

19.04.2017	14:44	282.960	WinSCP.com	; console, only, recommended
19.04.2017	14:44	18.905.808	WinSCP.exe..	; console and GUI
29.05.2017	18:40	14.497	WinSCP.ini	

to a path directory (C:\util\ in our case).

WinSCP.com will put you to the command line client. Play with it starting with 'help', 'open', 'close' and 'exit'. Some find WinSCP a bit bitchy on first encounter. If so, don't give up – once you master it you won't miss it any more.

winscp.com /script=progTransWin

will transfer two programs from the actual directory to a (Raspberry Pi) target machine by the WinSCP script progTransWin. Scripts can be parametrised and in the end used in a generalised transfer recipe in a (Eclipse) C make project; see the downloadable examples given in Part II.

```
# transfer two programs to the target machine
# Copyright 2017 Albrecht Weinert      weinert-automation.de
# $Revision: 2 $ ($Date: 2017-05-29 18:37:55 +0200 (Mo, 29 Mai 2017) $)
open sftp://pi:raspberrypi@192.168.89.67
cd bin
option batch continue
option confirm off
put rdGnBlinkBlink -preservetime -permissions=775
put rdGnSimpleBlink -preservetime -permissions=755
exit
```

Listing 1: WinSCP script “progTransWin” for batch transfer of two programs (see Part II).

Note: Looking at the Part II example's enhanced script and the make file you'll notice again that we found no Linux/Ubuntu equivalent. No free FTP command line tool for Linux with WinSCP's flexibility, features and professional quality seems to exist.

We tend to blame our incapacity to search for such Linux programs. On the other hand we even found expert comments saying WinSCP is keeping them with Windows.

What we got as nearest to WinSCP's elegance – and have often used for automated FTP transfers from Linux servers in the past – is LFTP. On the other hand recurring fingerprint/certificate refusals with sftp:// and parametrising with make variables may drive you nuts – all this a no topic with WinSCP.

On Windows 10 a single file may be transferred by command line:

```
scp notes.txt pi@pi61:/home/pi/notes/
```

As there's no option to give scp the password that command is hardly usable for batch processing. But therefore we are quite happy with WinSCP scripts.

Transferring to non-home

Consider the automated (i.e. scripted and parametrised) file transfers with WinSCP shown in Part II (in the demo project). Extending the automated application deployment to the target Raspberry a bit further leads to the deployment of (updated) libraries and hence to scripted writing to non home/non user pi's directories like e.g. /usr/local/lib/ for libraries.

Playing with WinSCP as pi:raspberrypi we can see, list etc. those directories (or at least a part or them) but can't write. And we can't remote sudo in WinSCP.com.

To enable WinSCP remote transferring to non home/pi directories is to give root a password and enable root to ssh log-in. Then we can

```
winscp.com root:rudolpf@192.168.89.67
```

from command-line or within a script. To enable this do:

```
sudo passwd root
sudo nano /etc/ssh/sshd_config
```

At the entry PermitRootLogin exchange the setting without-password by yes.

Note: The usual setting “without-password” is misleading. It does not allow root log-in without password; it forbids log-in even if you have one.

Note 2: Giving root a password to log in has security implications. Consider which networks your Raspberry is in and which influence it has on others or process IO.

Note 3: The scheme works with other (non root) name:password, too, when having the necessary rights on target directories and files.

SD-card: save and restore – and clone

For the basic installation we prepared the [μ]SD card with an installation image downloaded from a trusted source on an Ubuntu PC/laptop by the dd tool:

```
sudo dd of=/dev/mmcblk0 if=2017-04-10-raspbian-jessie-lite.img bs=4M
```

This copies a disc image (from the .img file) to the target card.

On a Windows PC we can do this interactive with the “Disc Imager” (figure 2 on page 5):

```
C:\util\ImageWriter\Win32DiskImager.exe
```

Consider the facts:

- Those tools do also work the other way round.
- A Raspberry's [μ]SD card is its one and only SSD (in all standard configurations).

Hence, both tools seem good for complete save, restore or copy/port to other Raspberry devices in any installation or working state. To save the actual state

- shutdown and un-power the Raspberry,
- remove the card and insert it to your laptop/PC

To save as .img file with Win32DiskImager chose a target file and click “Read”, see [figure 2 on page 5](#)). This file may not exist, just chose an appropriate name.

To write the saved image to another SD-card, one of the same size as the original should work. A bigger one is always usable, and has to be used if Win32DiskImager complains on size.

Warning: When hitting the size problem and ticking “write anyway” Win32DiskImager crashes. And that brings the drive used (G: in fig. 3) in an unusable and not removable state.

Two caveats for Win32DiskImager:

- Do not go ahead when getting complains on the operation planned. Click exit.
- Be extremely careful with “Write”.
Double check the drive letter. Win32DiskImager can kill your system.

For the size problem as such Win32DiskImager is not to be blamed. Using dd with the same cards on an Ubuntu laptop finishes the read and write operations (taking ages) without crashing but complaining on the non fitting size afterwards. The card then will start to boot in the Raspberry but will grind to a halt on root sector errors. Doubling the SD-card size does the job – as with Windows. And reducing the -bs size is of no avail.

Note: On the Ubuntu laptop recognising a SD-card – both directly or, cause of not fitting slot, via an USB adapter – seems sheer luck. The device may neither appear in lsblk nor fdisk. In the USB case the command lsusb sometimes may trigger the recognition.

The following commands should save the current state of a Raspberry in an .img file.

When putting the card in an USB adapter it may appear as sdb when having luck or when the lsusb triggering works:

```
sudo fdisk -l
lsusb
sudo fdisk -l
sudo dd of=~ /FHpis/61/Noob4lite41b_may17.img if=/dev/sdb bs=4M
sudo dd if=~ /FHpis/61/Noob4lite41b_may17.img of=/dev/mmcblk0 bs=4M
```

The write command to μSD (mmcblk0) comes to an end without complains with larger cards. In will fail with “fitting” size in exactly the same cases when the Win32DiskImager would complain.

The size problem

The root cause of the “size problem” most probably is no two [μ]SD-cards having the same size. By “use all rest of card as drive” in raspi-config this deficiency is made virulent. This setting should just be omitted.

The too clever dd problem

When cloning a µSD-card with dd, the obvious way is via an .img file with two dd commands with interchanged 'if=' and 'of='; example:

```
sudo dd of=~ /FHpis/61/Noob4lite41b_may17.img if=/dev/mmcblk0 bs=4M
# remove source card, insert destination card
sudo dd if=~ /FHpis/61/Noob4lite41b_may17.img of=/dev/mmcblk0 bs=4M
```

Especially when

- + using two cards of same size and type and
- + getting no complains and seeing an .img file of fitting size produced ... –

then one intuitively expects a one to one / bit by bit clone. But. alas, dd more than often does not do this. In the worst case it "thinks in files" and omits some.

It seems dd just is not an imager: dd is trying too cleverly to consider partitions files and rights. In all such bad cases where dd failed – sometimes in a quite perfidious way – and with the same cards and devices we never had problems with Win32DiskImager on Windows.

Part I's results

We can install a usable OS and tools on Raspberries. Having provided all basic tools and having set up communication we can administer Pis with some comfort remotely from both Ubuntu and Windows. And we can save and restore the current state of our "little servers", sometimes a bit spoiled by the "size problem".

Comparing Raspian lite=GUI-less with with the GUI variant we see with the GUI variant

- ▼ 39+ more services/processes running immediately after reboot
- ▼ 2..5 s delay when typing on putty after a short pause.
On a GUI-less/lite we always saw an immediate reaction as we type.

And when it comes to even modest real time requirements (see page 47) with PLC like cyclic 1 ms tasks (threads) we experience no problems with Pi3 and lite, while the GUI variant reproducibly always and totally fails in latencies and many other aspects:

- ▼ The GUI variant showed just slightly worse latency results (sometimes max. latency above 200µs compared to always below 200µs on lite).

This seems not so bad for the non lite graphical OS. But:

- ▲ Loads (ping etc.) have no measurable effect on the lite Raspian.significantly increase
- ▼ Such loads, on the other hand, significantly increase he latency on the GUI variant.
And the latency literally "explodes" by just moving the mouse.

Hence we must state the GUI making the Raspberry/Raspbian unsuitable for any embedded/ realtime/ server work. This resembles our experiences in the large with "real" servers ([29]).

Part II is ordered as for a beginner's path:

- get simple process output to work
- get to know some IO libraries and approaches
- making process control programs lock resources / inhibit double start
- making process control programs start automatically / as services
- switch from local compile and build to
- fully grown (C C++) cross platform development with Eclipse, SVN (or GIT) and all the comfort a full grown workstation can yield.
- extending the range of sensors and actuators usable with a Pi
- exploring (open, standard) protocols for distribution.

When reading PART II one may take appropriate short cuts and omit the points already done and understood as well as libraries or approaches one won't use anyway.

P A R T II

Pin	GPIO	funct.		funct.	GPIO	Pin
01		3,3V	o o	+5V		02
03	02	sda	o o	+5V		04
05	03	clk.1	o o	Gnd		06
07	04	pud	o o	Tx	14	08
09		Gnd	o o	Rx	15	10
11	17		o o	pwm	18	12
13	27		o o	Gnd		14
15	22		o o		23	16
17		3,3V	o o		24	18
19	10	MOSI	o o	Gnd		20
21	09	MISO	o o		25	22
23	11	CLK	o o		08	24
25		Gnd	o o		07	26
27		ID_SD	o o	ID_CC		28
29	05		o o	Gnd		30
31	06		o o	pwm	12	32
33	13	pwm	o o	Gnd		34
35	19	pwm	o o		16	36
37	26		o o		20	38
39		Gnd	o o		21	40

Fig. 3: Raspberry Pi's 40 pin two-row IO connector

Using the GPIOs

Our first goal is to use Raspberry's GPIO pins in an own program, preferably written in C. In any case, we want our programs cross-compiled (cross-made, cross-build) from comfortable workstations – on Windows. For a minimal proof of concept we start with binary output using a small set-up with two LEDs, driven directly by the Raspberry's μ P, see figure 4. For more IO and to connect a logic analyser etc. one may use a breakout board, see figure 5 on page 31.

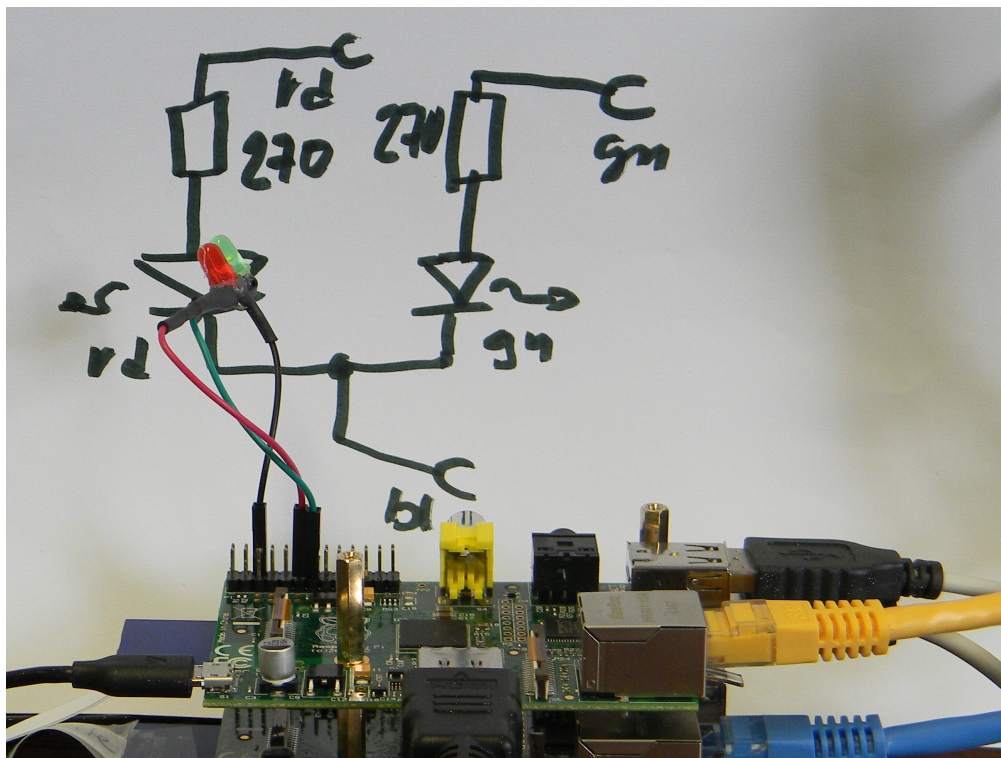


Fig. 4: Minimal IO

First of all we would need a library to use Raspberry Pi's IO pins. WiringPi is a well known one, and it is used by piXtend (see below). So, let's use wiringPi just for start.

But, in the end we will **not** favour wiringPi and switch to pigpio[d] (page 29) for all applications. We prefer thinking in "real" pin or GPIO numbers; see figure 3.

And, for "real" process IO applications, we always take Pi3's or better, contrary to figure 4.

The following recipe gets git and wiringPi, builds the latter and makes it known to the C compiler:

```
sudo apt-get update
sudo apt-get install git-core
git clone git://git.drogon.net/wiringPi
cd wiringPi          # you may keep this directory for reference
./build
```

Now we make a directory `~/bin` which will be on our path (`$PATH`) after next re-boot, a work directory `~/progWork` and an empty C source file.

```
mkdir ~/bin && mkdir ~/progWork
cd ~/progWork
touch rdGnBlinkBlink.c
```

To work on the C source `rdGnBlinkBlink.c` and other text files located on the Raspberry, we best use FileZilla and Editpad on our Windows PC. N.b. this is for a tiny first experiment, only. In the end, for real development, we'll use Eclipse and cross-build by make (tool) on the PC.

Hint, warning: When having finished and stored the `.c` source in Editpad, FileZilla will ask if it should propagate the changes. Do not forget to make FileZilla do so.

```
// A first program for Raspberry's GPIO pins
// Rev. 0.7 17.05.2017 Copyright (c) Albrecht Weinert
// weinert-automation.de          a-weinert.de
// It uses two pins as output assuming two LEDs connected to as H=on
// wiringPi[0] (GPIO17/17): red
// wiringPi[2] (GPIO21/27): green

// This program forces application singleton and may be used as service
// Compile on Pi by:  g++ rdGnBlinkBlink.c -o rdGnBlinkBlink -lwiringPi

#include <wiringPi.h> // pinMode, digitalWrite
#include <stdio.h>     // perror
#include <unistd.h>    // close
#include <fcntl.h>     // O_RDWR
#include <sys/file.h>  // flock
#include <signal.h>    // signal SIGTERM
#include <stdlib.h>    // atexit

int lockFd;
// The following file must exist for this program to start work
// Make the lock file by: touch /home/pi/bin/.lockRdGrBlinkBlink
char const * const fileSpec = "/home/pi/bin/.lockRdGrBlinkBlink";
// So, deleting this file inhibits the start even by cron etc.

static void onSign(int s){
    if (s == SIGINT)  exit(0); // cntl C terminates normally
    exit(s);
} // onSign(int)

static void onExit(void){
    pinMode(0, INPUT); // release red LED pin
    pinMode(2, INPUT); // green LED pin
    flock(lockFd, LOCK_UN);
    close(lockFd);
```

```
} // onExit()

int main(){
    if ((lockFd = open(fileSpec, O_RDWR, 0666)) < 0) {
        perror("can't open lock file (must exist)");
        return 97;
    } // can't open lock file (must exist)

    if (flock(lockFd, LOCK_EX | LOCK_NB) < 0) {
        perror("can't lock lock file (other instance running)");
        close (lockFd);
        return 98;
    } // can't lock lock file

    if (wiringPiSetup() == -1) { // initialise wiringPi (this is essential)
        perror("can't initialise IO handling (wiringPi)");
        flock(lockFd, LOCK_UN);
        close(lockFd);
        return 99;
    } // can't initialise wiringPi

    atexit(onExit); // register exit hook
    signal(SIGTERM, onSign); // signal hook
    signal(SIGABRT, onSign);
    signal(SIGINT, onSign);
    signal(SIGQUIT, onSign);

    pinMode(0, OUTPUT); // red LED pin as output
    pinMode(2, OUTPUT); // green output

    while(1) {
        // red green
        digitalWrite(0, 1); // on
        delay(200); // 200 ms red
        digitalWrite(2, 1); // on
        delay(100); // 100 ms both
        digitalWrite(0, 0); // off
        delay(100); // 100 ms green
        digitalWrite(2, 0); // off
        delay(200); // 200 ms dark
    } // while endless loop
    // 600 ms sum
} // main()
```

Listing 2: The first process IO program to start with.

After finishing the work on the .c source (listing 2) translate it:

```
cd ~/progWork      # already there
g++ rdGnBlinkBlink.c -o rdGnBlinkBlink -lwiringPi
ls -l
```

```
-rwxr-xr-x 1 pi pi 6816 Mai 13 12:37 rdGnBlinkBlink
-rw-r--r-- 1 pi pi 486 Mai 13 12:36 rdGnBlinkBlink.c
```

We see the source and the ready to run (executable) program. Run by:

```
touch /home/pi/bin/.lockRdGrBlinkBlink # make lock file
./rdGnBlinkBlink
```

The first line is necessary only once, as the program won't start when its lock file doesn't exist.

The ./ is necessary as our working directory is not on the path for executables (\$PATH). Copying it to a directory on the path will allow the start without extra ado from anywhere.

```
cp rdGnBlinkBlink ~/bin/      # do this as to have it on the PATH
```

Now it may be time to explain why our first output program seems so long and may look complicated – it's neither. A minimal “hello output” with exactly the same 600 ms loop, two LEDs blinking would be 16 non comment lines easily excerpted from listing 2, see listing 3 (page 18).

But according to this paper's title our requirements are a bit higher.

As the germ cell of of a full grown 24/7 process control / IO application we at least want it to

- clean up and leave a specified, controlled output state when finishing or being killed and
- prevent more than one instance of such control program running (i.e. be a singleton).

Note: A watchdog as third point will be introduced below (page 52).

Comparing listing 3 and 2 shows the “process control” part implementing the external behaviour being exactly the same. The complication by those two – minimal by the way – requirements will not grow substantially when getting to a full grown process control program and will partly be put in extra sources and include + source files (or may be even own libraries).

Using this small excerpt (listing 3), it will easily get evident or demonstrated that running two instances of the program would spoil the timing behaviour on the process outputs. These multiple starts happen quite easily, often by implementing automatic starts (boot, cron, etc.) forgetting one already has one. Additionally well meaning users tend to start control applications without noticing their state.

Process control applications, in almost all cases, must not run in more than one instance and should enforce this by themselves: No more than one application must access process related IO.

The Unix style solution in listing 2 is to use a fixed lock file that has to exist. Before entering any process control part including its initialisation, it is tried to lock that file. If it can't be locked or if it does not exist the program terminates. As a welcome side effect we can delete the lock file to prevent all future starts – those by hand as well as the automatic ones.

Of course, the lock file must be unlocked when program ends – no matter why or how the program was terminated or killed. Hence, we best implement a clean up and put the unlock there.

This clean up, we need anyway – so it's no extra complication for the unlock. When controlling process outputs it is essential to bring them in a specified state when the program ends, no matter how and why. In listing 2 this is done by catching the relevant signals (interrupts) as well as the program end and putting the clean-up in the registered hooks. In our case we release (and de-energise) our outputs, which most often is the adequate procedure.

```
// A first simple program for Raspberry's GPIO pins
// Rev. 0.0 17.05.2017 Copyright (c) Albrecht Weinert
// This is the simple (non process control) version of rdgnBlinkBlink
// see the comments there.
// compile by: g++ rdGnSimpleBlink.c -o rdGnSimpleBlink -lwiringPi
#include <wiringPi.h> // pinMode, digitalWrite
int main(){
    if (wiringPiSetup() == -1) return 99;
    pinMode(0, OUTPUT); // red LED pin as output
    pinMode(2, OUTPUT); // green output
    while(1) {           // red green
        digitalWrite(0, 1); // on
        delay(200);         //          200 ms red
        digitalWrite(2, 1); //          on
        delay(100);         //          100 ms both
        digitalWrite(0, 0); // off
        delay(100);         //          100 ms green
        digitalWrite(2, 0); //          off
        delay(200);         //          200 ms dark
    } // while endless    600 ms loop
} // main()
```

Listing 3: The simplified (non process IO) program – not to use just to play with.

An application as service

When using the Raspberry as server – or device – we usually want applications (process control, web server etc.) start automatically when powering up without a user having to login.

rc.local

One way is putting the starting command at the end of `/etc/rc.local`. For Listing 2 put

```
/home/pi/bin/rdGnBlinkBlink &
```

at the end (before the last line `exit 0`). Our small program will be started at the end of the boot process. Do not forget the `&` at the end. It puts the thing in the background goes on with the script.

To stop a program started this way, best determine the process ID number and kill it:

```
ps aux | grep rdGnBlinkBlink
sudo kill TheProcessNumberProvided
```

The process ID number is the second word in each line given by `ps aux`, hence lookup by:

```
ps aux | grep rdGnBlinkBlink | awk '{print $2}'
```

If valiant enough replace the number lookup by direct kill using this:

```
ps aux | grep rdGnBlinkBlink | awk '{print $2}' | xargs kill
```

or (if available) a "mass murderer" command

```
sudo killall rdGnBlinkBlink # also works on most raspbians
```

cron

The cron service knows an event “@reboot”. Add an @reboot line by

```
crontab -e # Note: when adding a program requiring sudo do: sudo crontab -e
```

```
@reboot /home/pi/bin/rdGnBlinkBlink
```

This works without & at the end at the cost of an extra shell process.

```
ps aux | grep rdGnBlinkBlink
```

reveals the extra (avoidable) shell process process, the can be killed.

```
pi 526    1912    388 ?    16:53    0:00    /bin/sh -c /home/pi/bin/rdGnBlinkBlink
pi 528    4008    1696 ?    16:53    0:00    /home/pi/bin/rdGnBlinkBlink
pi 818    4776    1916 S+   17:16    0:00    grep --color=auto rdGnBlinkBlink
```

As more complicated programs require other services (gpiod e.g.), DHCP settled etc. one would like to start them a certain time (35 s in example) after reboot:

```
@reboot sleep 35 && /home/pi/bin/hometersControl &
```

The cron service is probably running by default, but logging may not always be enabled. If something is doubtful or wrong when using cron, the first look would go to its log file, /var/log/cron.log by default. You might wish to enable cron logging by:

```
sudo nano /etc/rsyslog.conf
```

and put in or uncomment this line (found under # rules #):

```
cron.*                                /var/log/cron.log
```

In our above example cron would log e.g.

```
May 26 08:53:49 rasp67 CRON[499]: pi CMD (/home/pi/progWork/rdGnBlinkBlink)
```

It shows our program's start time and command, but only the shell's pld.

Mimic a service – start stop restart enable disable

With a bash script (listing 4) rdGnBlinkCntl we control our exemplary “process control” program (listing 2, ending page 16). Make the script by

```
cd ~/bin
touch rdGnBlinkCntl
chmod 755 rdGnBlinkCntl
```

and work on it via FileZilla and editpad.exe. Do not forget to set the Unix (LF only) option.

```
#!/bin/bash
showRdGnBlinkCntlVers () { echo "
# /usr/local/bin/rdGnBlinkCntl ~resp. ~/bin/rdGnBlinkCntl
# control the rdGnBlinkBlink service          V.01 22.05.2017
# (c) 2017 Albrecht Weinert          weinert-automation.de
"; }

rdGnBlinkCntlHelp () { echo "
# call by: rdGnBlinkCntl command
# commands are:
#   start | stop: start or stop rdGnBlinkBlink
#   restart:      stop and then start
#   disable:      inhibit the (next) start
```

```
# enable:      allow the service to be started
# version:    show version info
"; }

if [ "X--help" == "X$1" -o "X" == "X$1" ]; then
    showRdGnBlinkCntlVers
    rdGnBlinkCntlHelp
    exit 0
fi

if [ "--version" == "$1" -o "version" == "$1" ]; then
    showRdGnBlinkCntlVers
    exit 0
fi

progPath=/home/pi/bin/rdGnBlinkBlink
lockPath=/home/pi/bin/.lockRdGrBlinkBlink
searchPt=rdGnBlinkBlink

stop () {
    ps aux | grep ${searchPt} | awk '{print $2}' | xargs kill
}

start() {
    ${progPath} &
    pid=$!
    sleep .3
    if ps -p $pid > /dev/null; then
        echo "rdGnBlinkBlink started with PId ${pid}"
        exit 0
    fi
    exit 99
}

if [ "start" == "$1" ]; then start; fi
if [ "stop" == "$1" ]; then
    stop
    exit 0
fi

if [ "restart" == "$1" ]; then
    stop
    start
fi

if [ "enable" == "$1" ]; then
    if [ -f $lockPath ]; then
        echo "rdGnBlinkBlink was enabled, already."
        exit 0
    fi
    touch $lockPath
    exit $?
fi
```

```

if [ "disable" == "$1" ]; then
    if [ -f $lockPath ]; then
        rm $lockPath
        exit $?
    fi
    echo "rdGnBlinkBlink was disabled, already."
    exit 0
fi
rdGnBlinkCntlHelp

```

Listing 4: Script rdGnBlinkCntl to control rdGnBlinkBlink (listing 2) as service.

For testing you may use:

```

gpio readall
rdGnBlinkCntl start
gpio readall
rdGnBlinkCntl stop
gpio readall

```

	BCM	wPi	Name	ModeStart	Val	ModeStop	V	Physical	..
	17	0	wPi 0	OUT	rd	IN	0	11 12	..
	27	2	wPi 2	OUT	gn	IN	0	13 14	..

When rdGnBlinkBlink is running you should see GPIO 17&27 as OUT and when stopped as IN.

Cross-compile C for Raspberry from a powerful workstation

Now we have our first GPIO C example (rdGnBlinkBlink, listing 2, page 16), do know how to implement it as a service (listing 4) not counting the play variant (rdGnSimpleBlink, listing 3). This is a base to go further to useful and greater projects.

But now its also time to pause and re-think tooling and deployment:

- Very few of us like to develop software on a GUI-less Raspberry with local tools and stone age editors (nano being the least evil of them).
- Or when adding the GUI, only few would like to handle IDEs, version control, documentation generators and all else on a Raspberry (besides from having killed even modest real time capabilities).
- At least this will be true for those of us using all this and more in all comfort and speed on their powerful Windows (or Ubuntu) workstations.

The pre-condition to develop C software for Raspberries on Windows is being able to cross-compile and cross-link; all else will in the end build upon this. To get this going we download

23.05.2017	17:06	773.928.611	raspberrypi-gcc4.9.2-r4.exe
------------	-------	-------------	-----------------------------

from <http://gnutoolchains.com/raspberry/>. Run it to

- install at C:\util\WinRaspi• usable for all
- make no links for duplicate files
(we're on Windows, links exist and do work there, but no one will expect seeing one)
- add C:\util\WinRaspi\bin\ to the path or let installer do it
(In this case it makes sense, even if no one likes extending the PATH by every install.)

To test this best make an empty directory,

- get our .c source (rdGnBlinkBlink.c, listing 2) there via FileZilla and
- cd there.

Then command

```
arm-linux-gnueabi-g++ rdGnBlinkBlink.c -o rdGnBlinkBlink  
-lwiringPi
```

You'll get the compiled and linked runnable `rdGnBlinkBlink` in no time.
Transfer it to the Raspberry, best to another directory, `cd` there and

```
chmod 755 rdGnBlinkBlink  
rdGnBlinkBlink &
```

It works! A program made on a Windows PC does GPIO on a Raspberry.

`arm-linux-gnueabi-g++` is the equivalent to plain `g++` we used on the Raspberries.

Note on the prefix `arm-linux-gnueabi`:

These prefixes are used for cross tool-chains: They are unique prefixes for the target processor (architecture) and specific library sets. When setting up a new cross compile project in your GCC enabled Eclipse you will be asked for a tool path and a tool prefix; here it would be:

`C:\util\WinRaspi\bin` and "`arm-linux-gnueabi`", the latter standing for
`architecture-noVendor-system-applicationBinaryInterface`.

Note on the `g++` or `gcc` choice:

Both `gcc` and `g++` are GNU compilers respectively tool-chain drivers, doing almost the same. `g++` treats `.c` files as C++ source while `gcc` expects and handles plain C. In the case of our IO example, listing 2, both work and both produce an executable of almost the same length and content.

To round up our (Windows) cross-compile tools chain we should also have a make file understanding at least `make clean` and `make all`. See more in chapter Eclipse – make project (p. 23).

A note on Windows:

Evidently, we prefer Windows on powerful development workstations – with Java, Eclipse, Open-Office, GNU tools, SVN etc.. We have all liberty of open tools, while enjoying Windows' comfort and professionalism: domain and network file system integration, decent powerful explorer (not changing its name with every upgrade) with tool integration e.g. for SVN (tortoise), decent text editors (Editpad), common clipboard support and so on. And (almost) all just works fine.

With Ubuntu, more than once, dragging files to shells suddenly stopped them working or changed its behaviour. Unclear, pure text or no clipboard support is a good recipe to drive Linux users mad. Experiencing regular total crashes or losing tools on upgrades of Ubuntu and derivatives, one is constantly shooed back to Windows.

Well this happiness with Windows plus open tools did last up to Windows 7 professional respectively Windows Server 2008 R2 enterprise. Windows 10 changed this: It is slow (on the same hardware) and unreliable. The less controllable updates / upgrades do render installed tools inoperable. Nevertheless, even when finally forced to "upgrade" to W10 professional, we stayed there. The good news is: All our tools (Eclipse, Gnu, SVN, Git, Java, doxygen, AVR, ...) were kept working and even the Explorer was still SVN aware.

On the other hand, almost all what we describe for Windows can be (and was) done on a powerful Ubuntu workstation, too. And yes: Most Windows (even <10) have their "drive nuts" potential, for example, the dangerous faking directory and file names, when accessing the file system graphically, like showing the fake name like "Programme" for "Program Files".

But the comfort, stability and usability balance is positive. Hence use Windows, Ubuntu or what ever you like as your cross-development platform.

Eclipse – step zero

Now (cross-) developing on a powerful workstation, we want, the comfort of a powerful IDE. Our choice is Eclipse, used since years for Java projects, Web, AVR C and much more.

Put your sources, listing 2 & 3 in our examples, in an Eclipse's project folder. Best make the folder and the copies before making the new cross-C project. Set tool path and a tool prefix to:

C:\util\WinRasp\bin and arm-linux-gnueabihf- and

... enjoy Eclipse's support and comfort.

Well, a little work on make files and project setting will be unavoidable.

A reliable source of trouble are Eclipse's automatically generated make files, which notoriously fail. Before stepping into (great!) configuration trouble to get this make**2 working, drop the generated make files and write an own makefile which Eclipse would use with targets all and clean.

Thereby you get

- + immense flexibility considering targets, devices and else which in the end will often be needed,
- + building and cleaning automatically by scripts or by just running make by command line without starting or even needing the IDE
- ▼ involved with the ill syntax and semantic of the make tool.

So we make the appropriate changes in project → settings C/C++ → build and add a makefile to the project. And while we're at it we put the exemplary project in a SVN repository.

Eclipse – make project

See at the browser or get (by svn checkout <https://...>) all makefiles, sources etc. at https://weinert-automation.de/svn/rasProject_01/

You'll need to login (guest:guest). In this paper ([31]) there will be no listing for

26.05.2017	15:27	9.521	makefile
26.05.2017	15:27	1.163	make_raspberry_01_settings.mk
26.05.2017	15:27	1.165	make_raspberry_02_settings.mk
26.05.2017	15:27	1.116	make_raspberry_03_settings.mk
29.05.2017	18:37	770	progTransWin and some more

The makefile and its includes do work in our Eclipse C make project via project build and clean.

As postulated above you can use the makefile with more functionality and in automated batch files directly (without Eclipse). Be in the project/source directory, say D:\eclipCeWsOx\rasProject_01:

```
make help
make help_comm
make clean all
make PROGRAM=rdGnSimpleBlink clean all
```

The last two commands generate our two Raspberry IO programs rdGnBlinkBlink (PROGRAM default setting) and rdGnSimpleBlink (listings 2 and 3). And by

```
winscp.com /script=progTransWin /parameter pi:rasberry
192.168.89.67 bin rdGnBlinkBlink

winscp.com /script=progTransWin /parameter pi:rasberry
192.168.89.67 bin rdGnSimpleBlink
```

we transfer them to /home/pi/bin of our Raspberry 192.168.89.67 with the parametrised WinSCP script. This is integrated in the makefile and can (on Windows, only) also be done by:

```
make PROGRAM=rdGnBlinkBlink clean progapp
make PROGRAM=rdGnSimpleBlink clean progapp
```

Eclipse – troubles and hints

SVN client chaos

Where there is light, there is shadow

One trouble with Eclipse is putting in a decent SVN client. Subclipse made the least trouble. And there's the problem of different client versions with in-compatibly different structures of local working copies. You may well have two or more SVN clients on your workstation: command line, TortoiseSVN (explorer plug-in), Subclipse (eclipse plug in) etc. If the SVN client's working copy versions differ, your in trouble as virtually no SVN client offers backward compatibility. Tips:

- Try to keep the number low (≤ 3). At least, the three SVN clients named share settings.
- Do not try to stay with old working copy versions too long. Have a strategy when and how to update all your clients and to upgrade all local working copies (with batch files).

Eclipse marking non-existent errors

This problem hits mainly, but not only, cross-compile, cross-build projects:

While “make all” directly on the shell or indirectly in Eclipse by “build project” goes with zero errors and warnings – and yields a usable result – Eclipse marks the sources with non-existent errors and warnings and loads of red and yellow marks where none should be. Additionally, “open declaration”, language searches and else are giving incorrect results:

So we have an IDE bugged useless.

With CDT (C/C++ plug in) Eclipse uses own partly buggy compilers and indexers for judging sources. The “real” make uses the “real” target compilers etc., the only ones one has to make happy anyway. Web search reveals a lot of in-official remedies concerning configuration and partly source code, some of them useless or harmful and some also sheer voodoo.

One tip is to check Eclipse's include path configuration, even if it was correct yesterday. It has to “include” all local includes (those of -I./include e.g.) and all implicit includes the target tool chain has. So far, so good. But even if this is correct, errors of this category (uint8_t not defined) may remain. It seems CDT/indexer being unable to handle indirect and conditional includes correctly. Sometimes, this include handling problem may be worked around by mucking up respectively spoiling the sources a bit by extra includes, as already seen in this quite old AVR example:

```
#include "arch/config.h" // for sake of Eclipse (4.2.x)
#include "we-aut_sys/ll_system.h"
#include "pt/pt.h" // Eclipse 4.2.x; can't handle nested includes
#include <avr/io.h> // for sake of Eclipse; 4.7 is even worse with includes
#include <stdint.h> // for sake of Eclipse (needed since 4.7)
```

Problem remains that those extra unconditional includes (done when needed in other include files) may, besides the aesthetic damage, under certain conditions spoil the real build. So, sometimes unexplainable errors remain or can't be voodooed away. This was the case in our Raspberry C projects when growing beyond our introductory examples. Here updating to the newest Eclipse CDT IDE – then Oxygen, 4.7.0, June 2017 – was the rescue. Adding Subclipse 1.10.13 and Web-tools (for some .xml, .css for doxygen e.g.) was no problem and brought the projects back to no errors/warnings/red/yellow.

But before getting too enthusiastic on Oxygen: For the AVR projects in the same workspace updating to Oxygen (with AVR plug-in) led to a not repairable (not voodooable) catastrophic failure with > 1000 false errors.

Note 1: To emphasize the last point, projects used successfully for years got a useless IDE by just upgrading Eclipse. Eclipse versions good for one target may fail on others, and vice versa. Hence, never ever spoil a running IDE by updating. And, at least, save every bit (installations and workspaces, full trees etc.) before daring an update to have a safe way back.

Note 2: An IDE for C or any other language marking false errors and warning is worse than none. When marking errors or warnings, correctness and consistency with the target tool chain has the absolute top priority. Speed – wrong answers fast – is less than secondary.

Note 3 (begin 2019): Switching to “Eclipse IDE for C/C++ Developers; Version: 2018-12 (4.10.0)” and just using on (copy of) workspace and projects went without troubles and most of the indexer / false positive troubles gone. we started to remove above voodoo step by step.

To avoid too much enthusiasm Eclipse2018-12 insisted on a HTML syntax check of Doxygen control files – they are NOT html – leading to loads of warnings. Exclude Doxygen files from build is the remedy.

Cross tooling summary

Now we can successfully

- cross-compile/cross-develop with
 - GNU-tools,
 - using Eclipse (Oxygen) with make, or
 - make (alone / on shell or automated batch)
- bringing all under version control, here SVN
- having all on Windows (or almost all on Ubuntu)

for our Raspberries and even

- upload the program just build from Windows to the Raspberry automated by make.

So, we can go on to utilise Raspberry Pis in a professional development environment.

Making a library

Having common utility functions, variables and values in extra .c and .h files, let's see how to make a library from them, when not wanting to link them to every executable in question.

In a key matrix example below we have three sources

- `keysPiGpioTest.c` main program
- `weRasp/sysUtil.c`, `include/sysUtil.h` utilities and cyclic task execution support
- `weRasp/weGPIOd.c`, `include/weGPIOd.h` IO support for using the `gpio` library

The usual (and no bad) way is to translate all three .c files to .o files best organised in the makefile and link all three .o files to the executable keysPiGpioTest.

With this procedure you may

- change every source before cross-build. The good point is
- get one monolithic (mid-sized) executable you may transfer to and
- run on every Pi where a pigpiod is installed and the daemon is running.

On the other hand you may be tempted to make one (or more) of the utilities a library – in our exemplary case `sysUtil.c`. Doing so you may

- separate stable utility and runtime code from the more volatile application sources
- keep the source code away from the application programmers.

Then you translate one source less and link the extra library with the `-lsysUtil` option. The library `libsysUtil` has to be present on the cross-build workstation as well as on every Pi where an application linked against it must run. To make the library on the workstation do:

```
arm-linux-gnueabihf-gcc -Wall -DMCU=BCM2837 -I./include -shared  
-o weRasp/libSysUtil.so -fPIC weRasp/sysUtil.c
```

```
copy weRasp\libSysUtil.so C:\util\WinRaspi\
    arm-linux-gnueabihf\sysroot\usr\lib\
```

Transfer the library libSysUtil.so to the Raspberry to a directory remotely (ftp) accessible.

There do:

```
sudo cp libsysUtil.so /usr/local/lib/
sudo chmod +x /usr/local/lib/libSysUtil.so
sudo ldconfig
```

Starting with GPIO – a look at wiringPi, bcm2835 and a derivative

We here survey and test some GPIO libraries starting with bcm2835 and wiringPi, as basically used already on page 15, above. See or `svn checkout` as described on page 23.

Hint: In the end we use pigpio[d] for all real applications and strongly recommend to use this route. Skip to page 29 to avoid the sightseeing detour.

Still here? Then install the bcm2835 library on the Raspberry by

```
wget http://www.airspayce.com/mikem/bcm2835/bcm2835-1.50.tar.gz
tar xvfz bcm2835-1.50.tar.gz
cd bcm2835-1.50/
dir
./configure
make
sudo make install
find / -name "libbcm2835.a"
```

To test the just installed library locally on the Raspberry translate and run the lib's examples/blink

```
cd ~/progWork
cp /home/pi/bcm2835-1.50/examples/blink/blink.c ./
g++ blink.c -o blink -lbcm2835
./blink
```

This little program `blink` In will flash the red LED on our test harness (figure 4, page 14).

To use the library for cross-compile and -make it on our (Windows) workstation and projects, too, we copy two of the library's files

02.06.2017	11:47	80.726	bcm2835.h
02.06.2017	13:03	63.756	libbcm2835.a

to our workstation. The `.h` file goes to the tool chain's include directory

```
C:\util\WinRaspi\arm-linux-gnueabihf\sysroot\usr\include\bcm2835.h
```

The `'a'` file – the compiled library – we will add to our Raspberry cross tool-chain, as

```
C:\util\WinRaspi\arm-linux-gnueabihf\sysroot\usr\lib\libbcm2835.a
```

At this stage you should be able to translate a little example by command line:

```
arm-linux-gnueabihf-g++.exe blink7.c -o blink7 -lbcm2835
```

The executable `blink7`, made for Raspberry target would run there, but not on Windows.

The projects example `rdGnBlinkBlink` (listing 2, page 16, based on wiringPi) has been ported as `rdGnBcm2835Blink` to `libbcm2835` and is fully supported by the makefile – including the program transfer to the Raspberry. See the SVN project for details (cf. page 23).

The `bcm2835` library is a bit heavyweight compared to its features and suffers from its initialisation ritual. We tried to improve, and reduce this library to a minimum. While the improvement was promising, we could not reduce respectively get rid of the initialisation part. The GPIO usage initialisation is a bit tricky and described more by tradition as by specification. In the end we gave up this approach as well as using this library at all.

So far we considered the Raspberry IO libraries

- wiringPi
- bcm2835
- pigpio (see below on page 29).

And we used and tried them in small process IO applications – forcing singleton, being usable as service etc. – like seen and discussed with listing 2 and 4.

wiringPi – resume

wiringPi, well known and widely used, tries to cover the range of Raspberry Pi1, 2 and 3 with its diverse variants as well as some alternate Pi lookalikes. Additionally wiringPi covers a wide selection of extension boards or so called “shields” considered popular. Well this “serve all” approach is doomed to fail under other than the assumed “what is all” conditions.

Probably as consequence of this “cover everything” approach wiringPi introduces indirection respectively abstraction layers away from the μ P's (BCM2835, BCM2836, BCM2837) GPIO numbers or (virtualised) IO register addresses ([56]).

With wiringPi's indirection one can directly refer to the 28 respectively 40 pin header numbering or an own special wiringPi numbering scheme. One example:

Pin header 28 / 40 Pi1 / most else	μ P BCM2835 (Pi1)	μ P BCM2837 / μ P BCM2836 Pi3 / most else	wiringPi's own number
HW pin 13	GPIO 21	GPIO 27	2

This scheme seems unique; all other libraries utilised use the GPIO numbers that refer to the μ P's “truth”, the pin's properties and available functionality etc. For a shield or extension occupying all 40/28 header pins in a fixed layout the HW pin number describes the extension's interface. wiringPi is eager to support that directly; all others may do the translation directly or better by some macros to get all flexibility for the targets to cover.

All this makes wiringPi quite big. On the other hand programs for wiringPi are the smallest binaries. They rely on finding their library ready (as .so file which is a Linux equivalent of .dll files):

```
lrwxrwxrwx 1 root staff      libwiringPiDev.so -> libwiringPiDev.so.2.44
-rwxr-xr-x 1 root staff 28420 2017-05-17 10:51 libwiringPiDev.so.2.44
lrwxrwxrwx 1 root staff      libwiringPi.so -> libwiringPi.so.2.44
-rwxr-xr-x 1 root staff 70288 2017-05-17 10:51 libwiringPi.so.2.44
```

On the Windows workstation for cross-compiling you'll find those files with the tool-chain in e.g.:

c:\util\WinRaspi\arm-linux-gnueabihf\sysroot\usr\lib

For further analysis we put the sources of wiringPi and other IO libraries in an extra Eclipse (GCC make) project.

The resume for libwiringPi:

- quite large 100 .c files and 52 .h (with examples)
- compiles (by Eclipse / make) only after some 20 corrections in the source files about 6 more corrections brought us to “no warnings”, too
- seems to adapt automatically to all types of Raspberries respectively BCM μ Ps
- relatively small executables (< 9kB for rdGnBlinkBlink, listing 2, page 16)

bcm2835 – resume

The bcm2835 library, as the name suggests, just handles and refers to the μ P's GPIO. As the name does not suggest, it is also usable for BCM2837 and the Pi3 (and others).

Note: Regrettably a lot of bcm2835 documentation stops between 2012 and 2014 with the Pi1, leaving the rest to forums and speculation. Having switched totally to pigpiod (2017 - 2020), we haven't investigated other libraries further.

Referring to just GPIO in the end makes bcm2835 comparably simple and small. The library is linked to the executable making those quite large compared to a wiringPi variant of equal functionality. On the other hand, an executable cross-made on Windows can be FTP-transferred to all Pis and run without having to install anything. To resume libbcm2835:

- relatively small 1 .c + 1 h. (w/o examples)
- compiles as downloaded with just two style warnings and no errors on Eclipse / workstation
- the library is written for Raspberry Pi1 / BCM2835, only
- this can be repaired (so far for Raspberry Pi3) by not using the lib's enums or macros for GPIO numbers – but own macros or direct literals
- large executables compared to wiringPi (~57kB for rdGnBcm2835Blink)
Note: large executables may run faster than small ones using a shared library.
- needs no library installation on a target Raspberry when transferring cross-builds

On the other bcm2835 seems outdated and to need

- stylistic improvements I
 - like typos comments but first of all
 - making indices out of all relative addresses to get rid of “y[x/4] respectively y + x/4 in production code

Trying this on base of the sources showed the result wouldn't be worth the toils. Hence, still profiting from the lessons learned, we deleted this sub-project and .the original bcm2835 and do not recommended to use it.

Let's see – and use – Joan N.N.'s pigpio[d] instead ([61]).

The pigpio library

As said above, pigpio is our preferred approach to Raspberry IO and the only one we use for real control applications.

All IO libraries considered so far do burden the production code using them with the trouble to

- initialise the GPIO (memory) usage,
- adapt to changing (virtual) addresses as well as
- the fighting with access rights

or in a certain sense worse

- giving users the rights for standard IO with all libraries and approaches.

No decent OS offering any protection will let user code do IO, as did former Raspbians. With them accessing GPIO required sudo. In between the standard GPIO usages (read write) can be made accessible without sudo, but more settings or alternate functions may not not.

Note: Standard IO without sudo works, when user (pi) is in group gpio.

The pigpio library uses a different approach. It defines a server or daemon which does all initialisations and has control over all functions of the GPIOs used. This server has to be started with sudo to run forever in background. Programs doing (process) IO just communicate with the daemon by

- socket (as in example rdGnPiGpioDBlink and all our control applications) or by
- pipe (never used here).

Both approaches need no sudo. In the case of socket the control program and the GPIO pins may be on different Raspberries on the same network or one control program can use multiple Pi's IO.

A third way is

- linking the pigpio library to the program (as in example rdGnPiGpioBlink).

Note: This linking pigpio to the program approach is, of course, contradictory to pigpio's philosophy. And such program has to be started with sudo. But that program being build and run that way is also the said daemon/server. It can be used remotely or locally by other programs using the socket or pipe approach. And, not really surprising after all, pigpio forces its daemon's singleton property. A program using the link approach won't start when the daemon (pigpiod) is running.

Even when a pigpio daemon (piGpioD) is forced to be a singleton, nevertheless, one should make program's usage of pigpiod singleton, too. Additionally, when using only piGpioD one should exclude all other approaches from sudo-less IO if not yet so. On newer Raspbians this usually means removing user pi from group gpio by

```
sudo deluser pi gpio
```

But now let's go.

To get and install pigpio (see also [61]) do:

```
wget https://github.com/joan2937/pigpio/archive/master.zip
unzip master.zip
cd pigpio-master
make
sudo make install # see note *)
sudo ldconfig -v # may be necessary
```

Then do all the tests provided **) as recommended in [61] by

```
sudo ./x_pigpio # check C I/F (have a LED on pin 22 for fun)
sudo pigpiod # start daemon with default sample rate 5 µs etc.
./x_pigpiod_if2 # check C I/F to daemon (no or one fail)
./x_pigpio.py # check Python I/F to daemon (one or four fails)
./x_pigs # check pigs I/F to daemon (no or one fail)
./x_pipe # check pipe I/F to daemon (same result)
```

Note *): Since September 2020 this installation step ends with python error "No module distutils.core". All googled python remedies failed. Just ignore that error. piGpioD is installed and passes all tests.

Note **): Put a 1-active LED at pin 22 = GPIO25 to see some test steps working.

As said in [61] a few tests fail and many many more are passed; this is OK. We saw some irrelevant test fails on Pi3s with Jessie, Stretch but on Pi4 with Buster (May 2020) all tests pass.

N.b.: With 10 μ s sample rate some test will fail saying "half the expected number of yxz".

Please find and see the working examples rdGnPiGpioBlink and rdGnPiGpioDBlink in the SVN repository mentioned above on page 23.

And when using piGpioD on a Raspberry it's recommendable to start it at boot. Best use:

`sudo crontab -e # sudo here(!) but not at program to add; see note`
and add one line at the end:

```
@reboot /usr/local/bin/pigpiod -s 10
```

Starting pigpiod without parameters uses default settings: 5 μ s sample rate, PCM clock, port 8888, both interfaces (socket, pipe) enabled; -s 10 would set 10 μ s sampling; -p might change port.

Note: You must put sudo in front of crontab -e when adding or editing a task requiring sudo.

Do not prepend the task command with sudo. The crontab command suggests your editing a single system configuration file directly. That's an illusion. Every user and sudo seem has own cron editor settings and files. Getting this (sudo) business wrong is the source of notorious "My crontab (@boot) setting isn't ..." complaints.

Note also: cron tasks might be started without having your environment and path settings. Hence, use the full path to the "real" executable (like /usr/local/bin/pigpiod e.g.).

A last note: When cross-compiling / cross-building it may happen (after first time using a feature) linker ending with not being able to satisfy externals.

In this case the workstation tool chain may have other / outdated libraries.

```
23.06.2017 15:15          256.624  libpigpio.so
23.06.2017 15:15          65.128  libpigpiod_if.so
23.06.2017 15:15          75.624  libpigpiod_if2.so
```

Copy those files from the Raspberry's /usr/local/lib to your workstation's
C:\util\WinRaspi\arm-linux-gnueabi\sysroot\usr\lib (by ftp).

piGpioD does solve the GPIO sudo hassle. Additionally, Joan N.N.'s pigpio library has a lot of other rich and useful features, like PWM on every pin, a substitute for digital input interrupts and much more. (By the way: Do not use interrupts!)

This library's socket approach with all good points brings one disadvantage:

A (binary) single write to one pin takes 98 μ s on slow Pis. Making more than 4 such IO calls in a 1 ms cycle is, hence, a no go. Here, pigpio's bulk / bank functions can save the day.

Anyway, piGpioD, a programming style not endlessly repeating the same IO operation, like turning On a relay already on, is adequate.

The pigpio site (<http://abyz.co.uk/rpi/pigpio/pdf2.html>) offers no offline .pdf document. The very good on-line documentation allows being .pdf-printed (60 pages, links partly working).

The resume for piGpioD:

- It is our choice and
- service proofed in real time process IO applications running 24/7 over years
- good base for pure Java process IO applications without any extra libs; see [34].

Process IO hardware

In our introductory examples (rdGnBlinkBlink, listing 2, page 16) the process IO is directly connected to the IO pins of Raspberry's μ P. This might be feasible to a certain extent when all sensors and actuators are nearby in a closed encasement. Figure 5 shows a professional key matrix, LEDs and a piezo beeper as one suitable example for directly attached process IO. The break-out board is just a test harness and helps to connect a logic analyser – it is the black box with the green LED on the left of Figure 5).

In the production version the Pi is fixed to the back of the key matrix. When WLAN is sufficient, we now use a Zero in that place.

In all other cases and when controlling power beyond 48 mW, or distant actuators and sensors, interface and protecting circuitry is mandatory. Many offers are found, often under the genus “shield”. But beware of useless or outright dangerous devices.

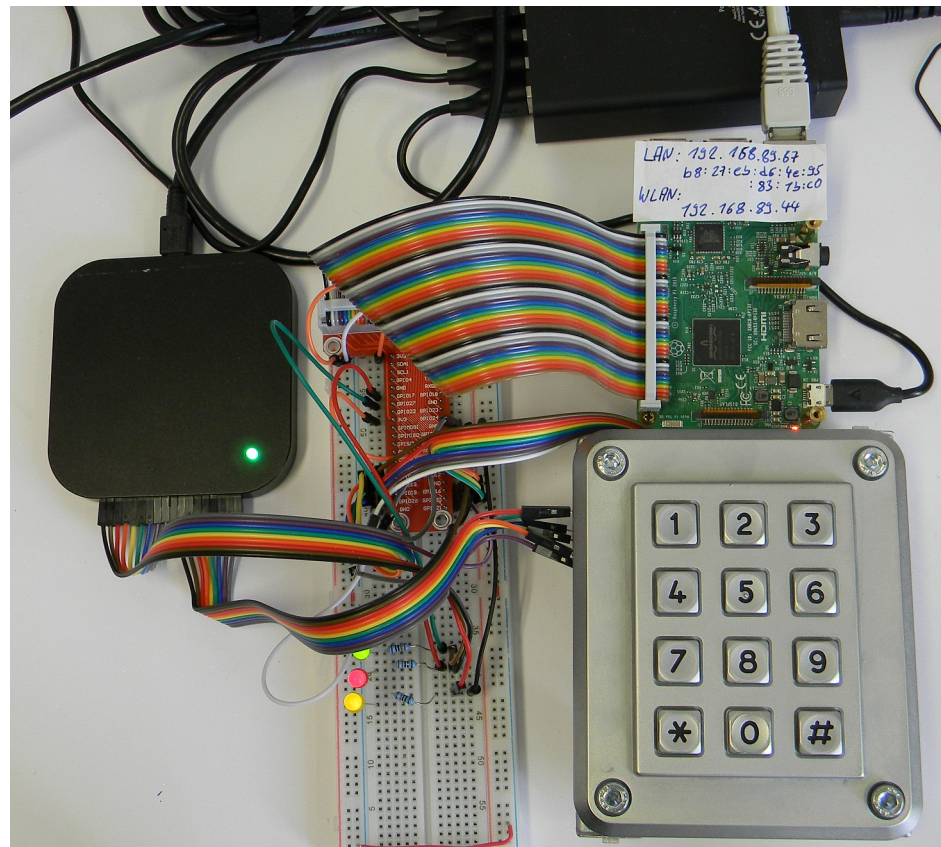


Fig. 5: Direct attached IO

LEDs and buttons – direct IO

As soon as process IO signals go beyond the borders of a Raspberry, protective circuitry is mandatory. Low power LEDs, piezo speakers and buttons within the same protective encasement are an acceptable exception.

A real use case had three LEDs and a 12 keys matrix EOZ Clavier S.series, 12 touches. Imagine the hardware set-up as figure 5 without the breadboard and logic analyser in in a flush-mounted wall box.

The key matrix was directly attached to 7 GPIO, see listing 5 for the concrete set-up. Single or multiple key presses were determined by a common algorithm. Its seven steps according to the seven scan lines defined in the keyMatrix structure were put in the 1ms cycle.

```

#define ROW123 PIN37
#define ROW456 PIN35
#define ROW789 PIN31
#define ROWa0h PIN33

#define COL147a PIN40
#define COL2580 PIN38
#define COL369h PIN36

#define NoCols 3 // number of columns
#define NoRows 4 // number of rows in key matrix

static keyMatrix thePad = {
    .noCols = NoCols, .noRows = NoRows,
    .colRow = {COL147a, COL2580, COL369h, // noCols + noRows,
               ROW123, ROW456, ROW789, ROWa0h},
    .keyVal= { // [ rowInd * noCols + colInd ]
        '1', '2', '3',
        '4', '5', '6',
        '7', '8', '9',
        '*', '0', '#'},
}; // thePad

```

Listing 5: Defines and structure for exemplary12 keys matrix EOZ Clavier S.series

The device's IO was controlled by a next tier system via Modbus over IP (see below). This general approach of using Raspberries as TCP/IP attached remote subsystems calls for a PoE solution.

LEDs directly driven by GPIO pins, of course, need a resistor of about 270 Ω and the pad drive strength can be reduced down to 8 mA.

Speakers and beepers

Piezo speakers should get a series resistor of e.g. 47 Ω , too. The drive strength may be set to 2 mA. Without resistor, one may observe spikes when applying voltage to the speaker, generated by its mechanical (resonance) vibrations. Magnetic speakers must be 200 Ω , with series resistors, when directly attached to one or two (push-pull) output pins.

For generating a tone one may use the gpio library's (see chapter The pigpio library, page 29) ability of "PWM at every pin, by setting the desired frequency and turning the tone on by PW=50 % and off by 0 %:

```

#define PIEPS PIN12

set_PWM_frequency(thePi, PIEPS, 400);
set_PWM_dutycycle(thePi, PIEPS, 128); // PW 50% → tone
:::::
set_PWM_dutycycle(thePi, PIEPS, 0); // PW 0% → silent

```

On the other hand, when not wanting to play music, but only giving little short signal beeps, one may produce good 500, 400 or (see example) 200 Hz beeps as little extra task in an 1 ms cycle thread, organising the matrix scan over a 10 ms period in 7 steps as main task:

```

while(commonRun) {
    waitKey1ms(3); // 3ms end scan
    gpio_write(thePi, PIEPS, beep);
    crScanStep(&thePad); // 0
    waitKey1ms(1); // 1ms scan
    crScanStep(&thePad); // 1
    waitKey1ms(1); // 1ms scan
    gpio_write(thePi, PIEPS, 0);
    crScanStep(&thePad); // 2
    waitKey1ms(1); // 1ms scan
    crScanStep(&thePad); // 3
    waitKey1ms(1); // 1ms scan
    crScanStep(&thePad); // 4
    waitKey1ms(1); // 1ms scan
    gpio_write(thePi, PIEPS, beep);
    :::::
}

```

As you assumed, the (uint8_t) variable beep is set to 1 when a tone is wanted and to 0 for off.

Using speakers directly controlled by GPIO brings two problems

- generating the tone frequency by software either directly or by hardware or library PWM may eat resources or may bring problems with seldom used and hardly tested library functions,
- depending on the surrounding and the speaker the tones may be just or hardly audible.

The last point may be healed by amplification instead of using a GPIO as power source – in simple cases one n-channel MOSFET alone may do the job.

Piezo buzzers, on the other hand, contain a fixed frequency generator circuitry and a piezo speaker of fitting resonance. With low electrical power – as deliverable from a GPIO – they can be quite loud. This solves both problems. On the other hand one has the fixed frequency, usually in the 2..3 kHz range. Nevertheless most type can very well by "on-off-modulated" up to about 600 Hz, allowing some distinguishable sound effects.

Relays

Relays are one way to handle remote actors, high power and to provide insulation.

But, you hardly find 3V relays with $\geq 200 \Omega$ coil resistance. And even if so

- the contact load of those very low power relays to control a three phase motor switch or three pole contactor and
- you need protective (diode) circuitry for Raspberry GPIO forbidding simple direct attachment in the end.

For switching some "real" power by GPIO one has to use either

- solid state relays with opto-coupler isolated control input or
- use transistor circuitry to switch relay coils.



Fig. 6: Eight relays module 10A

The break out board is used for attaching test equipment (logic analyser). It is omitted in production system, of course.

The latter solution comes quite handy in (semi-) professional modules, like in figure 6, page 34. Figure 6 shows a module with eight 5 V relays, each well controllable by Raspberry's GPIO. The separate 5 V supply for the relays may come from a separate source, or also from Raspberry's 5 V power supply including PoE. So in the end one has 11 short female / female pin to pin connections: Gnd, 3.3 V, 5 V and up to eight GPIOs between the Raspberry's 40 pin connector and the relay module (omitting, of course, the experimental break out board used in figure 6).

Figure 6 shows an eight relays module with 10 A changeover contact 250 V or 30 V=. Obviously it has one status LED per relay (which is quite nice) powered by the 3.3 V side (which is OK). These LEDs are red, which is the same mistake as with Raspberry's power LED:

In all professional process control standards red means error/fault.

On the other hand: Replacing Raspberry's red SMD LED "power and else OK" by a green one isn't difficult.

Besides this "red light sin" and besides being "no name and no documentation" the module is more than OK for less than 10 €. As the controller side and the 5 V relay supply have common ground (in most exemplars), the opto-couplers seem nice overkill (no circuit diagram available). And as a surprise in this undocumented case the 8 control inputs are low-active.

Similar higher power relay modules, best with 12 V coil supply, are also available and have been used in production systems.

Power transistors

N-channel power MOS-FETs or npn Darlington transistors or Darlington arrays may be used as (open drain, open collector) N-switches. The transistors would have to be placed near the Raspberry and can be connected directly (gate, array input) or via a fitting resistor (base) with a GPIO pin. (Load M and Raspberry's Gnd have to be the same than.)

Depending on the transistor / array type and a suitable supply and grounding layout, the loads may be in the range of 400mA ..10A and up to 60V. The loads and or their supplies can be placed quite separated from the Raspberry.

Note: There are (probably Chinese, totally undocumented) single power MOS-FET modules offering a red LED to signal on (nice, except the red) but featuring no current or temperature protection at all. Under certain circumstances, this might be acceptable.

Nevertheless, most power MOS-FETs, as the IRF520, need ≥ 4 V gate voltage to switch fully on. That a 3.3 V powered μ C cannot deliver.

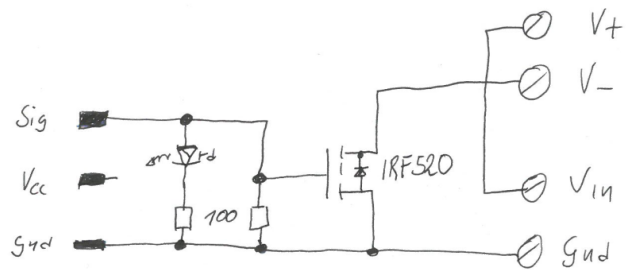


Fig. secret: Chinese module's circuit diagram

Replacing the power MOS-FET by a small signal one (if sufficient) heals the switching problem. And the 100 Ohm gate to source resistor and the LED can / should be omitted or made larger if gate on voltage is critical.

piXtend

In one case we used a piXtend board and stainless case to extend a Raspberry PI. For some 200 € piXtend tries to resemble the standards of industrial process control by featuring CAN bus, analogue IO etc., mostly with the help of extra AVR processors. If this mix fits well this sounds nice. But, the inter processor communication causes delays in an unbelievable order of magnitude.

piXtend offers graphical Codesys configuration/programming (for 50 € + VAT extra). Considering the advertising and the complete price including case, DIN rail bottom and Codesys license (all in all some 250 €), the properties as industrial PLC, the mechanical quality, the IDE etc. won't fill the customer with enthusiasm. Additionally the piXtend distribution ran into the infamous libc6 dependency disaster at one due update inhibiting all further update/upgrade at the Raspberry affected.

In the end this approach promised more than could be fulfilled. For a deep-rooted Codesys fan it might be attractive – but for all others we recommend not to use.

Communication hardware – RS485

As the Raspberry Pi3 has 4 USB, Ethernet, WLAN and with some extra software/configuration even Bluetooth, there's seldom need for extra communication modules. One exception may be Modbus (see page 39) over RS485 often found in quite interesting equipment, like heat pumps, gas ovens and "smart" power meters, to name just a few.

Note 1: Still, some industrial sectors are stuck at the communication hardware and protocols of the 1970ies.

Many DIN rail-mounted one or three phase energy meters come with a so called S0 bus. That's just an opto-coupler or switch output giving 300..1000 (as configured) 100 ms closures per kWh. These can be observed by Raspberry's GPIO input with pull-up and counted allowing seldom energy consumption updates and seldom and coarse non-equidistant average power samples.

Note 2: The so-called S0-"bus" is a good example of transferring century old technology's solutions without any cogitation to a new one: It's just counting and stopwatching the Ferraris wheel – but without being able to see the energy flow direction / sign of power.

Better electronic meters offer a real bus connection, usually at a slightly higher price. And, usually in this and some other fields, that will be Modbus over RS485. In the exemplary case of power meters this gives precise and actual measurements of voltage, current, frequency, active and reactive power, overall consumption and often much more.

Note 3: Despite note 1's criticism, using Modbus instead of the so called S0-protocoll is a big progress.

In our context, a process control set-up with those devices would be a RS485 bus with the Raspberry as Modbus master / client and one or many of those devices as Modbus slave / server.

RS485 is a serial two wire 5 V (3 V sufficient), push pull serial link, half-duplex, standard UART bytes + start and stop bits. To get the RS485 two wire interface one can

- use an USB to RS485 stick or
- attach a TTL to RS485 converter to Raspberries standard serial link:
UART: TxD = GPIO14 = pin8; RxD = GPIO15 = pin10

The USB stick solution on a Pi is commonly reported to bring driver problems and, if finally brought to work, causing resource conflicts and system crashes. The reason is Raspberry's USB link already being overused / misused for other build-in USB to XYZ converters.

Hence, one would stick with Raspberry's standard UART (pins 8 and 10). Besides having to deal with RS485 and Modbus related tasks, often, the sheer simple usage of this standard UART is more difficult than it should really be. This is partly due to no documentation at all or, worse, misleading documents and examples as not for which Pi variant they are applicable.

In short for the Pi3:

Enable serial interface and disable serial console by raspi-config or add

```
enable_uart=1
```

in /boot/config.txt (and reboot). This will bring Pins 8 (GPIO14) and 10 (GPIO15) as TX and RX in UART (i.e. ALT0) mode.

Do use /dev/ttyS0 – or the link /dev/serial0 – e.g. like:

```
int serHnd = serial_open(thePi, "/dev/ttyS0", ...
```

Using other devices (like the notorious /dev/ttyAMA0) with Pi3 will not work and, sometimes worse, will make the application or at least the thread grind to halt.

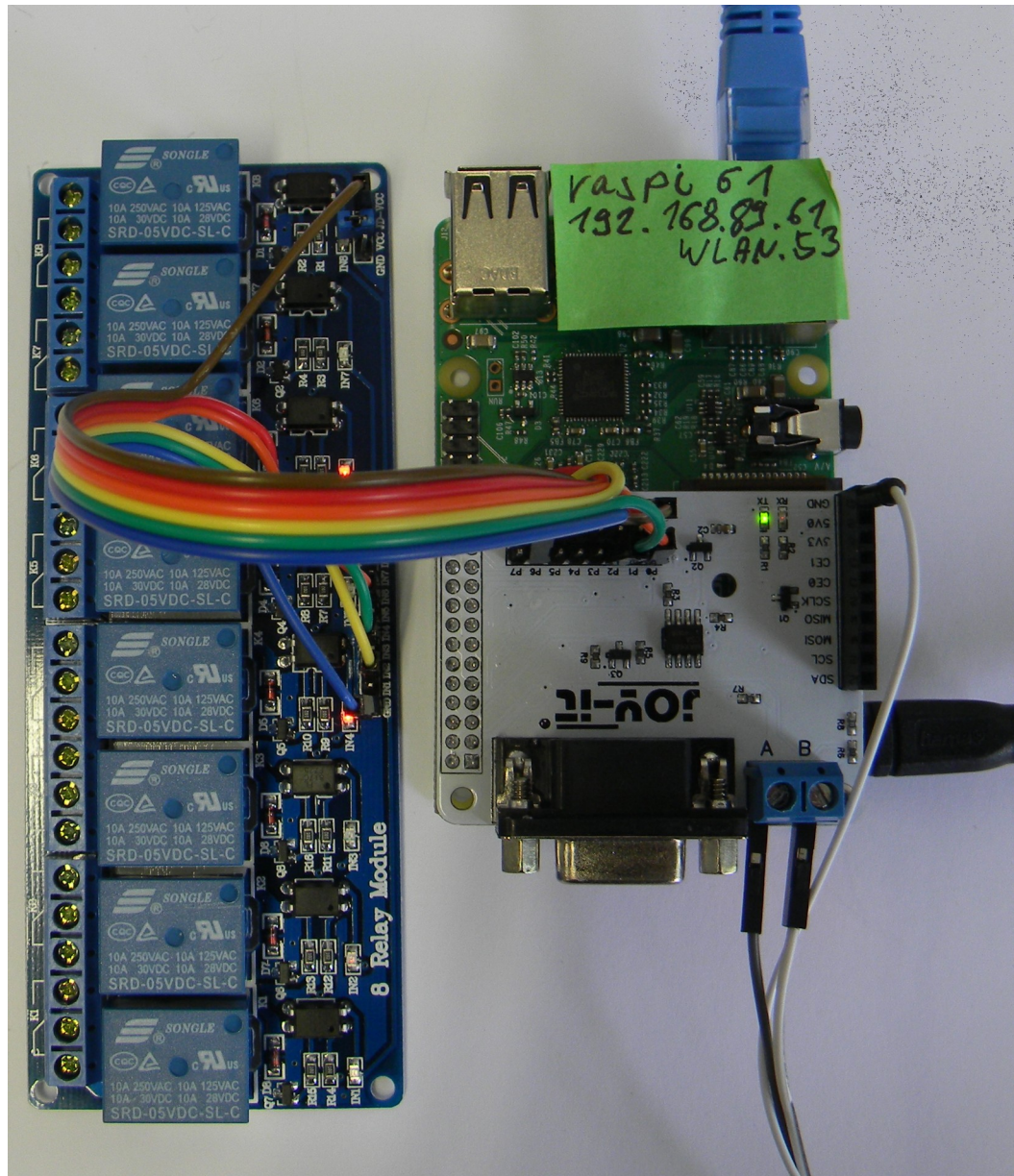


Fig. 7: RS485

module with automatic DE (for MAX485), i.e. transmit enable

When having the UART working, the TTL to RS485 solution as such is quite simple. In a minimal form, a MAX485 IC and three resistors would do it. Little modules of that simple kind are offered for less than one Euro. Do not confuse those with the much better module shown in figure 7.

Problem with the minimalistic “extra output for TE” solution (not figure 7) is:
One is left alone with the “transmit enable” signal = “DE” on MAX885.

As we have logically a one wire half-duplex bus, a sender must

- apply “transmit enable” before the first start bit of its (Modbus) telegram and
- remove “transmit enable” very shortly after the telegram's last stop bit.

Note: “Receive enable” might be the inverse of “transmit enable” or always on.

The suggesting idea of using an extra GPIO pin to DE is naive:

- we would have to modify the drivers respectively the Modbus library at every point, where the sending of a telegram would start.
- And while getting the start (even if error prone and killed by library updates) would be feasible, hitting the telegram's end usually evolves to a not solvable problem.

In the end, pure hardware solutions generating the DE signal from TxD are the most simple ones, and, when well made, totally reliable. There are modules of that kind, just translating RxD/TxD to/from RS485-A&B; see figure 7. Besides doing the TTL to RS485 transmission line (A&B)

translation by a MAX485 IC (with auto DE) for about 10 €, it brings all covered GPIO and supply pins to extra connectors. Hence other (process) IO can still easily be attached.

Note: This should be taken for granted but is not. There are “shields” with the 40 pin connector using supply and two IO – and burying all else.

Figure 7, page 37, shows this "GPIO forwarding" for a relays module as example.

The Plus side of this module ("joy-it") is

- the reliable DE automatism and
- forwarding the Raspberry pins unused for RS485 to an own pin header supplying an eight relay module and serving eight GPIO (outputs) to it

The (big) minus is

- ▼ having only two screw clamps "A" and "B". One always needs "Gnd", too!

Remark: In all our Modbus applications we used the module shown in figure 7. A version with two sets of “A, B, Gnd” screw clamps instead of the never used sub-D would have been ideal.

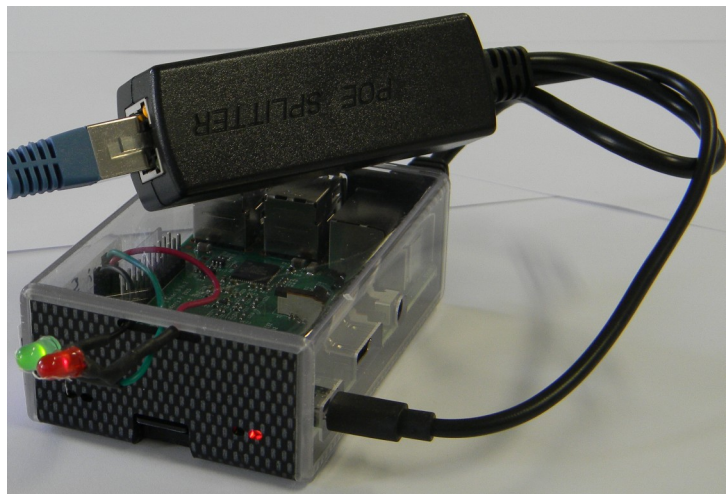


Fig. 8: Power over Ethernet splitter 5V, 2A

PoE – power over Ethernet

Switches, a key matrix in our example, low power LEDs and speakers / beepers and the Pi camera are peripherals one may use without extras hardware interfaces, often called shields. A Raspberry with such direct attached IO is put at its place of action and often controlled via LAN. Then getting the 5 V, 1.3 A is an extra complication, often underestimated. IEEE 802.3af Ethernet switches can provide power over Ethernet (36..57 V, up to 25 W). An on site step down converter then provides the 5V.

IEEE 802af splitters to "LAN without power" and "5V on microUSB", see figure 8, page 38, are on purchase for 10 €. We have some of those in 24/7 use without any problems. And do not use non IEEE “solutions” without converter to feed the 5 V from the other end of the LAN cable.

Raspberry Pi3+ (the “+” is new) and the Pi4 promise PoE, but it has not. It requires a special extra module, as expensive as a Pi 3, and an extra patch cable from the bulky module to the Pi. Hence, the P4 PoE solution is in no way better and more expensive than the figure 8 approach.

Note: Modules of the kind show in the figure 8 are also available with a USB-C power supply connector for the Pi 4.

Communication

For Raspberries, the natural way to communicate with companions on the same process control tier, with servers on higher tiers or with HMI systems is IP via LAN or WLAN (best private).

Note: For micro-controllers, like e.g. most Atmel AVR boards, this is not their “natural” way.

Protocols

(W)LAN and TCP/IP communication is an integral part of Raspberries and most available OSs, including Raspbian lite and the like. A whole bunch of protocols, applications and libraries is available – from start or after a bit of apt-getting. This includes ftp, http, SCP, SSH, Telnet, rlogin and more.

For process control communication (mostly of IO values and usually time critical) one might be tempted to write own binary protocols. On base of the GCC socket library (sys/socket.h etc., [63]), this can be done. Often it's wiser to use a standard protocol, like Modbus or MQTT.

Modbus

Modbus [65], [66] is an industry standard protocol to communicate process IO data. It is in wide spread since its origins in 1979 by the PLC manufacturer Modicon (now Schneider). Until today it is supported by most small automation systems or PLCs. It started as a P2P RS323 link and was later extended to multi-slave/server by RS485 and also by TCP/IP. Like many beloved programming languages and protocols of such age, Modbus has a bundle of architectural flaws the worst of which were mistakes already in 1979:

▼ Modbus has no layer concept and mixes physics, transport and application in an unfortunate way. The standard makes inadequate references to concrete devices and their addressing idiosyncrasies. As none of those has any effect to the protocol and its telegrams, these references to “4xxx registers” and the like are a rich source of confusion. A lot of secondary literature just dwells on what belongs to the standard and what was meant by “only for a 984A/B/X machine”.

▼ Modbus has no data type concept worth the name. One type is “coil” [sic!] = copper wire for relays. This is a boolean forced to one bit. Modbus insists to transfer thousand of bits (sorry “coils”) starting at arbitrary odd bit addresses aligned to bytes – meaning RS232 bytes since 1979 and also TCP bytes a bit later. For a controller with only single bit shift machine instructions the load of shift instructions may be larger then the rest of the task.

The only other Modbus data type is called “register” which is just a 16 bit something. (Above transport Modbus knows no “byte”). Even when the application “thinks” in bytes, this “register” approach brings the full computational load and risk of errors by endianness handling – without doing any good for applications’ or devices’ 32 or 64 bit data.

For the serial interfaces RS232/485 – still in wide use for small controllers – Modbus won't use UART parity supplemented by a simple checksum. The standard requires a complex CRC telegram checksum. This overkill is, again, overcharging some controllers even with a clever double look-up algorithm, requiring low baud-rates just to reduce the telegram and CRC load. Serial Modbus has no control flow concept, but a set of pause and time-out requirements forcing modern buffered UARTs to low gear and requiring extra programming. Some newer Modbus implementations ignore these requirements by implementing “full speed” – the better ones at least documenting or advertising it. But beware: Other stations might just fail or intentionally reject to communicate with such non-conformer – and, alas, quite rightly so.

One last point: Modbus by itself has no security measures. This can and must be handled by using protected networks, only. Serial RS232/485 lines should always fall in that category (by physics). For the preferred TCP/IP use protected private LANs (or tunnels).

Resume: As it is a widespread industry standard we should use Modbus on TCP on our Raspberry servers. With the GCC libraries and the Ethernet port we have all infrastructure at hand.

Serial (RTU) Modbus mostly via RS485 should be avoided as long as all partner are linked via Ethernet. Additionally Raspberries would need extra hardware (like MAX485 modules, cf. figure 7, page 37) to have RS485. On the other hand there are many small process IO devices around with good value for money featuring RS485 Modbus interface, like smart meters. Linking those to a Raspberry opens a rich field of applications with professional process IO. So let's do Modbus – preferably Modbus TCP – but let's be open for RS485 if an interesting application calls for it.

libmodbus

With socket libraries ([63]) implementing a minimal Modbus subset (class 0, TCP IP e.g.) is relatively easy. Going to higher classes or other interfaces will get hard work. But even with small subsets it is re-inventing the wheel, considering age and wide use of the protocol. But looking for a reliable and conforming Modbus library for Raspberry (Linux) was harder than expected.

In the end we used Stéphane Raimbault's libmodbus. It is

- + function code complete
- + including even exotic function codes 17/11, which is
 - ▼ implemented in a useless way reporting fixed constants and always “PLC run”
- + available and tested on many platforms
- + in wide spread use. It has
- + implemented all interfaces, TCP/IP and serial,
- + using an (▼ inconsistent) abstraction layer concept for the interfaces. So the application software would be less affected should one switch from TCP to RS485.
- ▼ The library is a “heavy malloc user”, which gives bad feelings in connection with real time and process control.
Trying remedies by too naïve replacing malloced structure with fixed static ones may lead to funny conflicts with Raspberry's MMU (ARM, Linux activated).
- ▼ The well and comfortable working modbus_receive() modbus_reply() server function pair offers no (call back) interface to handle output just put and input just requested. Time permitting you'd have to bring all possible input before and get all possible output after receive().
- ▼ The linked multi-stage (C) pointing leads to horrible readability.
- ▼ There is no real documentation of the library's functions, the data structures, nor on semantics and architectural ideas.
- ▼ Besides some typos there are a few bugs inhibiting cross-compilation and Eclipse (F3) look-up. One Eclipse / compiler troubling example: In

```
static int response_exception(modbus_t *ctx, sft_t *sft,  
                             int exception_code, uint8_t *rsp, ..... const char * template, ...)
```

the name “template” is considered as keyword misuse by some tools – rename it “tempel”, e.g.

On the other hand, this little list may sound much worse than it is meant. The library just works – and quite well so. To resume: With libmodbus we do have the complete sources of an excellent library to work with – and to learn from.

Installation

The installation needs git-core; to install git see page 15.

Then do:

```
sudo apt-get update  
sudo apt-get upgrade  
sudo apt-get install -y autoconf libtool  
git clone https://github.com/stephane/libmodbus/  
cd libmodbus/  
dir
```



```
lrwxrwxrwx root staff 2017-07-21 libmodbus.so -> libmodbus.so.5.1.0
lrwxrwxrwx root staff 2017-07-21 libmodbus.so.5 -> libmodbus.so.5.1.0
-rwxr-xr-x root staff 123408 2017-07-21 14:32 libmodbus.so.5.1.0
```

Do not forget to say `sudo ldconfig` afterwards. Otherwise you might get incomprehensible errors when running (cross-build) Modbus applications on that machine or when linking there.

Just to run a cross-build Modbus program on another Raspberry an unlinked `libmodbus.so` ftp-transferred there should suffice. after copying it to `/usr/local/lib/`:

```
sudo cp libmodbus.so /usr/local/lib/          ## to where it belongs
sudo ln -s /usr/local/lib/libmodbus.so /usr/local/lib/libmodbus.so.5
sudo ldconfig                                ## what ever it does, never forget
```

But it turned out that for what ever reasons programs required `libmodbus.so.5` even when made with `-lmodbus`.

Compile and cross-build

After a successful installation or transfer one can locally compile:

```
cd ~/libmodbus/src/tests                      ## or where ever the .c source is
gcc modApp.c -o version -lmodbus
./modApp
```

To be able to cross-compile, cross-make and cross-build from our (Windows) workstation – and to use Eclipse there – we have to get the include files to

`C:\util\WinRaspi\arm-linux-gnueabi\hf\sysroot\usr\include`

```
22.07.2017 11:22          1.199 modbus-rtu.h
22.07.2017 11:22          1.373 modbus-tcp.h
22.07.2017 11:22          2.114 modbus-version.h
22.07.2017 11:22        10.912 modbus.h
```

One may also take

```
22.07.2017 21:37          3.405 modbus-private.h
22.07.2017 22:04          5.829 config.h
22.07.2017 11:22          1.627 modbus-rtu-private.h
22.07.2017 11:22          1.247 modbus-tcp-private.h
```

Those more “secret” .h files would be needed to (re-) build the library itself and when digging a bit deeper like building and using their data structures. It is no fault to take them from start.

And we need the get the (“un-linked”) file `libmodbus.so` (~120kB)

and put it to `C:\util\WinRaspi\arm-linux-gnueabi\hf\sysroot\usr\lib`

In a (test) project we let see Eclipse the library sources primarily to have “Open declaration / F3”.

And we let them make by a suitable make file. While make (from command line or by Eclipse's build project) is no problem for all library sources Eclipse's CDT complains:

```
Warning "Code Analysis Problem"
No break at the end of case  modbus.c      /rasProject_02/modbus      line 439
```

The well commented case fall through is OK. To get also Eclipse to 0 warnings one has to add a “`//no break`” comment.

Cross-compiling example:

```
arm-linux-gnueabi-gcc -c -I. -DPLATFORM=raspberry_03 -g -Wa,-adhlns=bau/tests/version.lst
tests/version.c -o bau/tests/version.o
```

Cross-build example:

```
arm-linux-gnueabi-gcc -I. -DPLATFORM=raspberry_03 -g -Wa,-adhlns=bau/tests/version.lst  
tests/version.c -lmodbus -o au/tests/version
```

libmodbus application and experience

Have a look at the SVN project or check it out as described above on page 23.

And see the examples

- `keysModTCP.c` a Modbus server doing real process IO, a key matrix (12 keys), 3 LEDs and one beeper (in an 1ms cycle) for
- `keysModClient.c` a Modbus client using the input and controlling the output (in an 100 ms cycle); both Modbus over TCP.
- `weRasp\weModbus.c, include\weModbus.h` support for Modbus (RS485) devices, especially smart meters in home automation projects.

MQTT

MQTT ([67]) is a TCP based lightweight M2M messaging protocol claiming to be simple, low network bandwidth and small code footprint. Notwithstanding those claims, at least the "simplicity" might be slightly doubted.

MQTT is quite successful and widely used in the world of IoT and home automation. Most sources and the protocol are open. The protocol specification is managed by OASIS. To use it, one has to have at least one broker service running on a machine reachable by all clients via TCP/IP. This machine may very well be a Raspberry Pi3 doing other process control work.

A client program producing events might publish those to the broker. The event message must be assigned to a "topic". Topics are to be organised hierarchically in a tree, e.g.

```
labExp/sweetHome/meters/alarms
labExp/sweetHome/hmi/actuators
```

The publisher might chose several service quality levels from "once or lost", "at least once or duplicated" to "just once" when delivering a message to the broker.

The broker would run by default with no security or encryption. It might be configured with an own user/password base for access to ports and topics, for requiring client certificates and for using TLS. Before jumping to those features in a closed, secured (W)LAN, one should consider these requirements hitting all clients at the site. Think twice before burdening our small Raspberry and ESPxy devices with complicated protocol extensions. But beware: This "have no fear" only holds until we open our private process control LAN to someone else.

A client program wanting to react respectively listen to events has to subscribe to one or more topics. Besides specific topics, like the two examples above, single level (+) and multi-level (#, at the end, only) wildcards can be used:

```
labExp/sweetHome/+/alarms
labExp/sweetHome/hmi/#
```

In a certain sense, we have the Listener respectively Observer pattern implemented by the MQTT broker to which clients (programs) can publish or subscribe. A client program may very well have both the publisher and the subscriber role.

A client may publish one special "last will" or "testament" (LWT) message. This message will be stored by the broker and pushed to subscribers, only, after this client failed or died. Criteria for death are network and MQTT protocol errors and time outs.

MQTT products

There are a lot of MQTT enabled products with good price performance ratio on the market:

- ESP8266 WiFi modules with MQTT
- single to quadruple power (relay) switches, optionally
 - 230V AC or 24V
 - DIN rail mounted or wall plug-in
- touch wall switches
- lamps / bulbs and a lot more.

As did Modbus with smart meters, pumps etc., MQTT opens another range of products as sensors and actuators for our process control Raspberries.

MOSQUITTO

A Raspberry Pi 3 doing a 24/7 process control task in ≥ 20 ms cycles may very well execute some extra services, like an Apache 2.4 server for HMI (see below, page 53) and a MQTT broker, when used judiciously and almost only related to the process control task.

Mosquitto is an open source implementation of a MQTT broker etc. by Eclipse. It seems to be the most used implementation for small and embedded systems, notwithstanding the lack of findable respectively usable documentation and manuals.

To install Mosquitto do:

```
sudo apt-get update & sudo apt-get upgrade
sudo apt-get install mosquitto
sudo service --status-all | grep mosq
```

The installation command brings the broker and starts it as service. The broker service can be switched off and on by:

```
sudo service mosquitto stop
sudo service mosquitto start
```

If we want the broker alone (on an extra Raspberry, e.g.) we would stop here. For having the client (test) program, the library and the include file we do:

```
sudo apt-get install mosquitto-clients
sudo apt-get install libmosquitto-dev
sudo ldconfig
```

To test our new broker we utilise the command-line clients, just installed:

```
mosquitto_sub -d -t labExp/sweetHome/#
```

-t precedes the topic(s) we subscribe to, and -d brings some extra (debug) messages, helping to get acquainted with the protocol. To see the broker and our (one) running subscriber working we publish a fitting message by command-line, best in another putty console:

```
mosquitto_pub -d -t labExp/sweetHome/alarm -m "bathtub overflow 3"
```

If all went well, our subscriber sees the event, immediately. When sending an off topic message

```
mosquitto_pub -d -t hello/world -m "Hello 11"
```

nothing should happen.

Note: Besides mosquitto_pub and mosquitto_sub, there's a third command-line tool mosquitto_passwd.

libmosquitto

By some searching and grepping (due to lack of documentation) one can see, we have just one library and one include file:

```
lrwxrwxrwx 1 root root 17 2017-05-29 07:21 libmosquitto.so
-> libmosquitto.so.1
-rw-r--r-- 1 root root 46960 2017-05-30 00:32 libmosquitto.so.1
-rw-r--r-- 1 root root 54155 2017-05-29 07:21 mosquitto.h
```

in /usr/lib/ respectively /usr/include/.

Compile and cross-build

To cross-compile and -build on the workstation we put the include file to

```
C:\util\WinRaspi\arm-linux-gnueabihf\sysroot\usr\include
```

and the library both as libmosquitto.so.1 as well as as "un-linked" (copied) libmosquitto.so to

```
C:\util\WinRaspi\arm-linux-gnueabihf\sysroot\usr\lib
```

That's it. As first test we make a simple publisher example (with accompanying make include) in Eclipse and having the make tool build it and ftp it to the target Raspberry, as usual. With this proof of feasibility – and when having understood the API ([69]), we can add MQTT functions (public and/or subscribe) to our C process control software. And we can make own small (ESP8266 based, e.g.) MQTT devices.

1-wire

1-wire is a bus and protocol specification allowing several IO devices attached to one IO pin. For one 1-wire bus on a Raspberry the default is GPIO04 (Pin7). Another pin may be configured as well as multiple 1-wire buses. But it is good practice to stick to the default, when feasible.

1-wire devices do have a Vcc pin for 3..5V; 5V is allowed even when the bus is limited to 3.3V. By providing a 4K7 pull up resistor to 3.3V one may even omit the Vcc wire to the devices; they can live from the "One" bus wire alone. This feature is called "parasite power" and done by feeding an internal Vcc buffer capacitor by both the external Vcc pin and the I/O bus pin via decoupling diodes. Available 1-wire ICs respectively devices are

- DS1822 thermometer -40°C..+125°C; 1mK resolution, accuracy 2K; the unique factory 64-Bit registration number starts with 0x28
- DS18B20 thermometer -40°C..+125°C; 1mK resolution, accuracy 0.5K (0x28)
- DS2430 small EEPROM (0x14)
- DS2408 8 very small power I/O pins (input or open drain; 0x29)
- DS2413 2 very small power I/O pins (input or open drain; 0x3A)

For process I/O with Raspberries the last two, "digital IO via 1-wire", are not attractive. Better use GPIO directly or have a ESP8266 module do it via MQTT. And here would be no use case for an extra EEPROM as we can hibernate data on the SD-card in the OS's file system.

With Raspberry's lack of analogue input, the 1-wire temperature sensors in all their variants may come very useful. One can have the cheap naked IC as well as a device with 1..5 m cable sealed in a stainless steel tube fitting in the standard thermometer inlets of industrial tanks. When heating water with surplus solar power, e.g., one should read the temperature for safety and comfort limits.

1-wire devices are accessed by Linux's device as file approach, the behaviour being implemented in kernel modules (.so files). From the application's point of view this approach is orthogonal to the "use I/O pins directly or driver software approach". To make it happen, i.e. have the files and the kernel modules for 1-wire + thermometer available, we have to (sudo) modify two configuration files. In /boot/config.txt add

```
dtoverlay=w1-gpio # or dtoverlay=w1-gpio,gpiopin=x
```

And in /etc/modules add

```
w1_gpio
w1_therm
```

After re-boot one 1-wire is ready on the default pin *1). When a sensor is connected find it by
`dir /sys/bus/w1/devices`

```
lrwxrwxrwx 2018-07-06 28-0199d507010c -> ../../devices/w1_bus_master1/28-0199d507010c
lrwxrwxrwx 2018-06-28 w1_bus_master1 -> ../../devices/w1_bus_master1
```

Here 28-0199d507010c points to a thermometer's directory named by the IC's unique serial number. We strongly recommend to resolve the symbolic links*2) down to the thermometer's file w1_slave for a real time application.

A read of this "file" reveals an actual measurement as a two line text:

```
7f 01 86 5d 7f ff 01 10 ea : crc=ea YES
7f 01 86 5d 7f ff 01 10 ea t=23937
```

If the first line ends with YES the last number in the second line is the temperature in m°C, that is 23.9°C in the example. If the file isn't readable or if YES is not on its (fixed) position consider it an error.

Note1 : If a GPIO is thus assigned to a kernel modul, never touch, configure, read nor write it directly. The pigpio(d) library / daemon promises not to do so when not explicitly requested by user code.

Note2 : Do not let time critical code follow symlinks. It's quite a task and probably not optimised away.

Real time

Every server doing work for others and every system doing process IO does so under timing requirements. These requirements should be well defined, best clearly documented with consistent numbers and units. In some fields of application and communities much of it may be considered as implicitly clear, be it by the industry's standards or by properties of the usual standard products seen as not disputable *).

If someone with PLC background says “someThingX is done in the 1ms cycle” following is implied:

- there is a trigger event by the underlying runtime every 1ms “exactly” ... well $\pm$
 - $\pm$ accuracy (timing oscillator / source, short time)
 - $\pm$ jitter (variability of delays/latency)
- there are 86.400.000 such trigger events per day “exactly” ... well $\pm$
 - $\pm$ accuracy (long time; can sometimes be made better than short time value)
 - + exactly 0 or exactly 1000 events per leap second (Google, Oracle & others: 0)
- “someThingX” will run at every such trigger event “exactly” ... well $\pm$
 - + 0..max delay/latency (meaning max. .. later but never ever too early)

We have “real time” when all those times and intervals are specified to the application's requirements and “hard real time” when one occurred violation is to be considered as application failure. While the absolute numbers are important the “hard” property has, per se, nothing to do with speed. You may replace 1ms by 1s in our mental example and relax other requirements but consider not having (86.400 + number of leap seconds) runs per day as a total failure. It may be astonishing how “hard” that can be in some environments.

Wanting PLC / cycles and with a Raspberry Pi3 / Raspbian lite doing just process IO and related communication we can well implement 1ms cycles. Going faster might work but would be daring. Note *) on common process IO standards: The same is true for hardware, electrical signals, maximum ratings of IO, EMC etc. Most sold IO extensions, shields etc. are totally inadequate in this respect.

Absolute timing

The approach in our very first LED blink examples (listing 2, ending page 16, e.g.)

```
do something
delay (relative from now)
do something
delay (relative from now)
... and so on
```

never gives an exact timing, how ever exact the delay function used may be. Believe it or use a good logic analyser to verify. The basic recipe to start the “do something” at exact intervals relative to one start point in time is to use absolute time steps as supported by listing 6's functions.

```
/* Absolute timer delay for the specified number of  $\mu$ s
 *
 * @return sleep's return value if of interest (0: uninterrupted)
 */
int timeStep(timTv * timer, unsigned int micros){
    timeAddNs(timer, (long)(micros) * 1000); // timer += micros
    return clock_nanosleep(CLOCK_ABS, TIMER_ABSTIME, timer, NULL);
} // delay(unsigned int)

/* Absolute timer initialisation
 *
 * This function initialises the time structure provided.
 * @param timer the time structure to be used (never NULL!)
 */
```

```
void timeInit(timTv * timer){
    clock_gettime(CLOCK_ABS, timer);
} // timeInit(timTv *)
```

Listing 6: Absolute time steps; excerpt from weRasp/sysUtil.c

This is also the base idea to get PLC like cyclic tasks. Here the execution time of “do something” including all jitter and latencies and considering it's CPU/core usage must be shorter than the (next) timing step.

Latency and accuracy

Of course, it is of no avail to offer 1ms cycles for process control, when the runtime's latency (in the sense of delays on signals) is in the same order of magnitude or worse. One common command line tool to check latency is cyclicttest. Get it by:

```
sudo apt-get install rt-tests
```

and run it by e.g.

```
sudo cyclicttest -l1000000 -m -n -a0 -t1 -p99 -i400 -h400 -q
```

After some patience on a Raspberry Pi3 with process and some communication load we get a typical result like

```
# Total: 0001000000
# Min Latencies: 00009 # observed 4...12µs
# Avg Latencies: 00012 # observed 11...13µs
# Max Latencies: 00062 # observed 70..118µs
# Histogram Overflows: 00000
```

The tool and its options as well as the interpretation of the results are non-trivial. Anyway, running the same test again will give comparable results on different run-times and loads. Experimental outcomes are:

- A well configured (specialised, single purpose, single use) Raspian lite on a Pi3 is suitable for hard real time process control and 1 ms cycles.
- A graphical (non lite) OS in all our use cases never was and never will be.
- Using interrupts may also overthrow predictable / acceptable latencies and kill inter thread co-operation. Refrain from using them.

The accuracy of the 1ms cycle may very well measured by precise logic analyser observing outputs by the 1ms cycle and derived longer ones ($n * 100\text{ms}$) over a long period. In one case we observed an hour being 144 ms too long ($\sim 3.5 \text{ s/d}$). By (excerpt)

```
absNanos1ms = 1000000 + vcoCorrNs;

while(commonRun) { // timing loop in main thread
    timeAddNs(&cyclmsEnd, absNanos1ms); // 1 ms time step
    clock_nanosleep(CLOCK_ABS, TIMER_ABSTIME, &cyclmsEnd, NULL);
    if (++msTo100Cnt >= 100) {
```

with the signed byte vcoCorrNs set to -40 we improved the accuracy by 2 orders of magnitude. This correction by a calculated fixed value worked very well over many days of uninterrupted use. So we can conclude this Pi3's quartz stability being excellent while the accuracy could be better.

Of course, this “hand-made” correction value is no practical solution for wide use. vcoCorrNs should be automatically determined by a phase locked loop (PLL) “voltage controlled oscillator” (VCO) algorithm against a precise time source, like NTP (available) or DCF77 (extra hardware). Note: In the (latest) Jessie distribution CLOCK_REALTIME and ABS_MONOTIME's clock will be NTP tuned. Own inventions as with earlierJessies (or libs) aren't required any longer.

NTP

For PLC like cyclic execution we offer an 1 ms, 10ms, 20 ms, 100 ms and 1 s cycle by library and

Cycles and threads

For PLC like cyclic execution we offer an 1 ms, 10ms, 20 ms, 100 ms and 1 s cycle by library and run time support organising the manager (supplied) as one thread. The cyclic tasks as well as other event triggered ones will have be supplied as user threads.

Note: In a minimal runtime for AVR μ Controllers we based a similar solution (proven 24/7 since over 9 years) on Adam Dunkel's protothreads.

As protothreads are well suited for GCC, they could have been used too. But with full grown Linux runtime on a multi-core processor it's strongly recommended to use the runtime's own threading (pthreads) and signalling system instead. See sysUtil.c and sysUtil.h after svn checkout.

Threading and synchronizing

As most process control must handle multiple asynchronous tasks we utilise Linux threads for

- several cyclic tasks
- a central task for organising all timing, i.e. organising
 - 1ms and 100ms cycles and multiples of it as well as
 - time and date
- all else tasks the application in question requires.

The main() method will have to parse arguments, provide all necessary resources and make and start all all those threads. It may serve as one of the threads – or just sit suspended waiting on the other threads end, as show in this excerpt:

```
int main(int argc, char * * argv){
    :::::
    for (;;) {
        ret = getopt_long (argc, argv, "h?v", longOptions, &optIndex);
        :::::
    } // for over options
    :::::
    if (theCyclistStart(290)) { ::::: } // start the central timing thread
    on_exit(onExit, NULL); // register exit hook
    signal(SIGTERM, onSignalStop); // register signal hook
    :::
    signal(SIGQUIT, onSignalStop);
    :::::
    ret = pthread_create(&threadRS485Mod, NULL, rs485ModThread, (void*) NULL);
    :::::
    ret = pthread_create(&threadRelays, NULL, relaysThread, (void*) NULL);
    :::
    if (theCyclistWaitEnd()) { ::::: } // wait for the other threads to end
    :::::
} // .main(int, char * *)
```

For full details see the project (by svn checkout) files sysUtil.c, sysUtil.h and hometersControl.c.

Preventing two or more threads to enter critical code – critical mostly by using common variables / structures or other resources – is done by a common mutex guarding the critical code:

```
int ret = pthread_mutex_lock(&comCycMutex); // under lock
::::: // the critical code
int ret2 = pthread_mutex_unlock(&comCycMutex); // release lock
```

Under such mutex lock a thread may wait (optionally with timeout) for a signal from another thread. The wait call implies an unlock and re-lock for the (necessarily) used mutex:

```
int ret = pthread_mutex_lock(&comCycMutex);           // under lock
::: // there may be optional critical code before and after the wait call
ret = pthread_cond_wait(&cycTask->cond, &comCycMutex); // unlock / lock
int ret2 = pthread_mutex_unlock(&comCycMutex);        // release lock
```

Under the same mutex lock another thread may send the signal to one or all waiting threads:

```
int ret = pthread_mutex_lock(&comCycMutex);           // under lock
ret r= pthread_cond_broadcast(&cykTask->cond); // wake threads; unlock / lock
int ret2 = pthread_mutex_unlock(&comCycMutex);        // release lock
```

Using this schema detailed in the example, threading and the necessary locking, synchronising and signalling is almost as easy as with Java. Limitations with Raspian are no `pthread_yield()` and no working thread priorities. The first, if needed, may be substituted by `sched_yield()` and the priorities by fine granular organising of the process control tasks.

Co-operating applications

Besides the cyclic and organisational tasks (threads) a process control application may have to handle HMI output, output to files (log, error, CSV etc.) and much more. Except for very small and simple applications it seems logical to move some (or all) tasks not belonging to core (real time) process control and process IO to other programs on the same computer.

On the minus or costs side are

- inter process communication

and implicitly or explicitly

- inter process synchronisation.

On the plus side we see

- the core process control / IO program getting smaller and
- kept as simple as possible,
- having less or ideally no start parameters (start options)
- being quite suitable to be started as background service (on boot)

and hence as depending on less resources (files, HMI and else)

- reliably running 24/7.

Shared memory

To get to this separation of tasks in several co-operating programs we need a bi-directional communication from/to the process control program and all its helper programs. Here we could use pipes, sockets or queues, e.g. But here the programs tend to be tightly coupled as consumer and producer and one's failure may severely impede the other. For processes on the same machine shared memory is the easiest way to avoid such hard dependencies and, as well, allow flexible one-to-some relations.

A good approach for our (process control) use case is to organise all

- process input,
- process output,
- command input and
- status output

in one well-formed structure (of structures) clearly defined and centrally modifiable in one .h-file common to the application – i.e. all its processes.

This common structure is held and actualised in shared memory. Each co-operating process may work on the shared memory or on a local copy. The latter approach – doing the work on a local copy and copying the parts needed / modified from / to the shared memory – is better in most

cases, as almost each access to shared memory will have to be done under mutual exclusion by semaphore (not to be confused with above mutex) lock.

As the memory is shared between the cooperating processes they need a common handle or name, which in Raspian is an arbitrary int (32bit). The same is true for the semaphore set. This should be put in a .h-file (svn checkout sweetHome.h) common to the co-operating processes:

```
#define ANZ_SEMAS 3          //!< Semaphore set of three (3..10)
#define SHARED_MEMORY_SIZE 512      //!< Shared memory size 512 bytes
#define SEMAPHORE_KEY 0xcaef1924    //!< Semaphore unique key "Käfig24"
#define SHARED_MEMORY_KEY 0xbaffe324 //!< Shared memory key "Buffer24"
// always include weShareMem.h after (!) having defined the four values above
#include "weShareMem.h"
```

Now a program can make or get the shared memory by:

```
int shMemId = -1; // shared memory ID (initialised none)

static valsSharMem_t * valsSharMem = (void *)-1; //!< pointer to shared memory
// ^ common data structure; ^ initialised as error/none

:::
shMemId = shmget((key_t)SHARED_MEMORY_KEY, SHARED_MEMORY_SIZE,
                0666 | IPC_CREAT);

if (shMemId == -1){ /* error handling */ }
shMemPt = shmat(shMemId, (void *)0, 0);
if (shMemPt == (void *)-1) { /* error handling */ }
```

This is best seen in more detail in the files (svn checkout) weShareMem.h and weShareMem.c showing the detaching and destroying of shared memory also.

Using the shared memory attached in a program is best done in one place by e.g.:

```
memcpy(valsSharMem, &valFilVal,
       sizeof(valFilVal_t)); // put process values for other programs
valsSharMem->copInCnt = copInCnt; // get commands from other program
```

Semaphore sets

Of course, this putting values to and getting values from shared memory has to be guarded by a semaphore of a common (shared) set. Additionally, having put values for other programs can be signalled to consuming programs by another semaphore of the set.

Mutexes are private to one program and act as binary locks between this program's threads. Between threads they guarantee exclusive access to variables and other resources. And, under such lock, signals between threads by (two) special functions to be used under lock only.

Contrary to mutexes semaphores

- come in a named shareable set to
- work between programs (processes) and
- are not binary but have a value range of 0..max (max is probably 96766 with Raspbian).

A basic usage of a semaphore is a binary lock when

- decrementing to 0 is the lock operation and
- incrementing (under lock!) to 1 is the unlock operation.

This works because the decrement blocks as long as the value is 0. The blocking hence waits until another process unlocks. As this can be quite long or worst case for ever the lock operation can and should be guarded by a timeout by using semtimedop() instead of semop(). See the handling (svn checkout) in weShareMem.c. and .h.

Such binary lock is used to guard the access to all co-operating programs shared memory using one (number 0) semaphore of the common set.

Our second usage is signalling from program to a limited maximum number (say 9) of other programs by using other semaphores (1...) of the common set. The other programs wait for the signal just as above by lock best with a generous timeout.

The signalling program (here under semaphore number 0 lock) uses `semctl()` (not `semop()`!) to set the signal semaphore(s) to the maximum number (say 9) of other waiting programs. Then their lock will immediately succeed. After a time agreed upon (say $T_{sig} = \text{shortest signal period} / 6$) the signalling program uses `semctl()` to set the signal semaphores (1..) to 0. After a successful signal lock the other programs will wait $> T_{sig}$ before waiting on the next signalling. (Again see the examples named above.)

Watchdog

A well written useful, cyclic etc. process control application should run 24/7 and normally will without trouble. But from time to time (about every three months in one of our former 2017 set-ups) it crashes. And with so few events the causes aren't easy to investigate. Probably it will be some semaphore / synchronizing "kuddelmuddel" *) when many HMI / CGI (see below) programs are active via shared memory and one of them (or its browser or its link) crash in an unfortunate instant. In very few cases we see unwanted (frozen) process outputs.

But always, good operation could be resumed by resetting the PI manually. And reset will bring well-designed process output to safe state.

As it is good practice in process control, this rescue reset should be done by an OS independent HW watchdog when the process control program's cyclic operation ceases. In our set-up we'd trigger such watchdog in every second run of the 1-s-cycle.

Luckily, the Pi's BCM processor has such watchdog, which is exposed under Linux as a device or pseudo file (just like the 1-wire approach). By `dir /dev/watchdog` we see:

```
crw----- 1 root root 10, 130 2018-08-16 17:17 /dev/watchdog
```

Writing any character except "V" to `/dev/watchdog` will trigger it, causing a reset after 16s, when not re-triggering it within this interval. Hence, triggering every 2 s, as said above, will fit. Writing a "V" will disarm the watchdog. The process control program would do this at normal shutdown and after (!) bringing all process output in idle (off) state.

As seen from the `dir` above, only root can write the watchdog device after every system start. As we won't sudo run the process control we'll have to

```
sudo chmod a+=rw /dev/watchdog
```

before starting process control. Hence, we sudo `crontab -e` the following:

```
@reboot sleep 5 && chmod a+=rw /dev/watchdog
```

to have after reboot

```
crw-rw-rw- 1 root root 10, 130 2018-08-16 17:17 /dev/watchdog
```

Finally place (distribute) the following lines of code at the appropriate places

```
static int watchdog;

watchdog = open("/dev/watchdog", O_RDWR); // in initProcessIO()

write(watchdog, "V", 1); // disarm watchdog
close(watchdog); // and close at releaseProcessIO()

write(watchdog, "X", 1); // in 1s cycle (every second time)
```

Final note *): After some system and library update/upgrade rounds the mystic freezing was gone by summer 2018. Nevertheless, with cyclic / real time process control a watchdog is a must.

Web interface

As stated and verified, we can do real time process control with down to 1ms cycle time with a Raspberry Pi 3 and latest Raspbian Jessie – as long as we do **not** install the OS with **graphical HMI**. In other words:

As long as we use the little machine as headless server all is well – and installing graphics spoils all real time cyclic process control endeavour. Linuxes on other OSs with graphics do this by dozens of busy extra services all running around. Adding graphics to a non graphical OS like Unix/Linux means adding a lot of foreign objects in various variants.

The standard remote access to our headless little machine is putty.

And yes: It is possible to write a console program (startable and usable via putty) that, as just described, waits on the signal, gets data via shared memory and displays them on the console. (See `hometersConsol.c` as example.) It is also possible to get input from such console program and put the values and commands via shared memory to the process control application.

This "remote HMI by putty as shared memory coupled application" works perfectly reliable – but looks as stone age as the nano editor. It is quite natural to want at least this functionality in a web interface, and this at least *) in the same local (W)LAN where the headless process control machine server can be puttyed. And this does work!

Note *): One may / should specify "only" instead of "at least" for safety, security or load reasons.

While graphics just by being installed without anybody doing anything locally nor X-remote impedes even modest real time applications, a slightly loaded running Apache 2.4 server has no measurable detrimental effect. [sic!]

At first sight this may seem astonishing. But this approach, used judiciously, only transfers small amounts of data – partly static and cacheable – and puts all rendering and graphical toils to the workstation, laptop, tablet, telephone or whatever web-client.

Hence, we use Apache 2.4 – on our process control Raspberry – for remote HMI.

Apache 2.4

Installation

Apache 2.4 is (apt-get) provided in a reasonable way since the Raspian Stretch distribution. If – and only if – we are in Jessie we edit `/etc/apt/sources.list` and add a second entry in one line

```
deb http://mirrordirector.raspbian.org/raspbian/ stretch main contrib non-free rpi
```

and we (sudo nano) edit `/etc/apt/preferences` and add three lines:

```
Package: *
Pin: release n=jessie
Pin-Priority: 600
```

This effectively forbids all apt-get activities to use the extra stretch entry to keep our Jessie installation intact. But the stretch distribution entry can explicitly activated by a `-t` option.

```
sudo apt-get update
sudo apt-get install -t stretch apache2  ## only on Jessie do this
sudo apt-get install apache2             ## on >= Stretch do just this
```

installs Apache 2.4 plus its notorious test page

```
-rw-r--r-- 1 root root 10701 2017-11-27 10:48 index.html
```

in `/var/www/html` which should work. (Just browse to the machine's root.)

Afterwards one (should) may comment out the "deb ... stretch ..." line.

When using Stretch or Buster, just `sudo apt-get install apache2`

Installing PHP7 (not recommended nor used in the end)

Libidinal, many people want PHP for all web server side programming, including simple CGI. In our process control projects we did all server side dynamic content generation, including AJAX with GCI and used C for GCI programs. Doing so, one stays consistently in one language and our cross compiling workstations with IDEs etc. Hence we very well can omit PHP and did so.

But if wanting PHP, use PHP7 with Apache 2.4 on Jessie:

```
sudo apt-get install -t stretch php7.0 php7.0-curl  
php7.0-gd php7.0-fpm php7.0-cli ## on stretch omit -t stretch
```

Check the PHP 7 installation by

```
php -v
```

```
PHP 7.0.19-1 (cli) (built: May 11 2017 14:04:47) ( NTS )  
Copyright (c) 1997-2017 The PHP Group  
Zend Engine v3.0.0, Copyright (c) 1998-2017 Zend Technologies  
    with Zend OPcache v7.0.19-1, Copyright (c) 1999-2017, by Zend  
Technologies
```

To get Apache acquainted with PHP do:

```
sudo a2enmod proxy_fcgi setenvif  
sudo a2enconf php7.0-fpm  
sudo service apache2 reload
```

By:

```
sudo nano /var/www/html/info.php
```

make a file with the following content:

```
<?php  
echo "The server ", gethostname(), " is online.<br />";  
phpinfo();  
?>
```

Browsing to this file in root (<http://myPi/info.php>) should work and provide more informations as one wants public.

If you went so far and then decide (wisely) not to use PHP at all, you may try:

```
sudo a2disconf php7.0-fpm  
sudo apt-get purge php7.*
```

Apache 2.4 configuration

To be able to seamlessly work on from development workstations we put all web content in user space, too. This would normally be seen as potentially critical, but this is a headless 24/7 process control server with usually just one user (besides root). So we do as *the* user (default pi):

```
mkdir -p ~/var/www/include/  
mkdir ~/var/www/cgi/  
cp /var/www/html/* ~/var/www/  
dir ~/var/www/
```

We make a new configuration by

```
sudo nano /etc/apache2/sites-available/meterPi.conf
```

```
<VirtualHost *:80>
    ServerAdmin webmaster@localhost
    DocumentRoot /home/pi/var/www
    ErrorLog ${APACHE_LOG_DIR}/error.log
    CustomLog ${APACHE_LOG_DIR}/access.log combined

    <Directory "/home/pi/var/www/cgi">
        Options ExecCGI
        SetHandler cgi-script
    </Directory>
</VirtualHost>
```

Note: The directory part is for CGI (next chapter). One may also add something like

```
ScriptAlias /cgi-bin/ /home/pi/bin/cgi/
```

just to switch off Apache's standard pseudo directory leading to /usr/lib/cgi-bin.
Then we add the following lines to /etc/apache2/apache2.conf:

```
<Directory /home/pi/var/www/>
    Options Indexes FollowSymLinks
    AllowOverride None
    Require all granted
</Directory>
```

And, almost finally, switch configurations by

```
sudo a2dissite 000-default
sudo a2ensite meterPi
sudo service apache2 restart
```

After having tested the new configuration with the .html and .php files copied above we might remove or replace them in ~/var/www/.

To sum up:

Configuring and handling Apache 2.4 in our little Raspberry machine feels the same as with big servers – no extra training curve.

Showing log files

Our process control programs will record events, values, errors etc. in text files. Show a listing of clickable files in browser by following addendum in /etc/apache2/sites-available/meterPi.conf:

```
Alias /logs /home/pi/logs
<Directory "/home/pi/logs">
    Require all granted
    Options +Indexes +FollowSymLinks
    IndexOptions FancyIndexing FoldersFirst IconsAreLinks
</Directory>
</VirtualHost>
```

If all your log files – and all other text files from your Apache are UTF-8 you may add the the line

```
AddDefaultCharset UTF-8
```

to an appropriate configuration file, like e.g. /etc/apache2/conf-available/charset.conf .

And while we're at aliases in meterPi.conf (or where the virtual host is defined): A directory /icons / at its document root will not work on newer Linux Apaches as they substitute /usr/share/apache2/icons/ for it deep in the source code or at least in a totally hidden base configuration. The only way to stop that undocumented "feature" seems a "back alias to document root in the virtual host in question:

```
Alias "/icons/" "/home/pi/var/www/icons/"
```

Nota bene: Almost all such changes will only be effective after

```
sudo service apache2 restart
```

And syntax errors in configuration files will inhibit Apache's start – and may be hard to detect. Forgetting restart and test after changes may kill the auto start of Apache on next re-boot.

GCI programs

As we did provide the basic CGI configuration for Apache 2.4, already, we just test it by a GCI program written C (greet.c) to deliver a simple complete html page:

```
#include <stdio.h>
int main(int argc, char * argv[]){
    printf("Content-type: text/html\n\n"); // Every CGI delivery starts with
    printf("Hello, World.");              // content-type followed by content
}
```

On the workstation cross-compile the program by

```
arm-linux-gnueabihf-gcc -o greet greet.c
```

and ftp the binary greet it to /home/pi/var/www/cgi/. Test is by browsing to <http://myPi/cgi/greet>.

```
sudo a2enmod cgi ## do not forget this
```

A more complete version of the greet.c program (listing 7) is to show the line of actions (and the ill URL coding) in more detail.

```
/**
 * \file greet.c
 *
 * $Revision: 76 $ ($Date: 2017-12-01 19:43:45 +0100 (Fr, 01. Dez 2017) $)
 * Copyright (c) 2017 Albrecht Weinert
 * weinert-automation.de a-weinert.de
 *
 * cross-compile by:
 *   arm-linux-gnueabihf-gcc -o greet greet.c
 * local compile by:
 *   gcc -o greet greet.c
 */
#include <stdio.h> // printf()
#include <stdlib.h> // getenv()
#include <string.h> // strstr()
#include <ctype.h> // isdigit()
#include <stdint.h> // uint32_t
int urlDecode(char * str){
```

```

char *ptr = str;
char c = * str;
char c2 = *++str;
for (; c; c = c2, c2 = *++str) {
    if (c != '%') { *ptr++ = c; continue; }
    char c3 = *(str + 1);
    if (!isxdigit(c2) || !isdigit(c3)) { *ptr++ = c; continue; }
    uint8_t v1 = c2 <= '9' ? c2 - '0' :
        (c2 <= 'F' ? c2 - 'A' + 10 : c2 - 'a' + 10);
    uint8_t v2 = c3 <= '9' ? c3 - '0' :
        (c3 <= 'F' ? c3 - 'A' + 10 : c3 - 'a' + 10);
    *ptr++ = (v1 << 4) | v2;
    str += 2;
    c2 = *str;
}
*ptr = '\0';
return 0;
} // urlDecode(char *)

int main(int argc, char * argv[]){
    printf("Content-type: text/html\n\n");
    printf("Hello World! <br />\n");
    char * browser = getenv("HTTP_USER_AGENT");
    if(browser != NULL) {
        if (strstr(browser, "Chrome") != NULL) {
            printf("You're using Chrome like most of use.<br />\n");
        } else {
            printf("Your browser: %s <br />\n", browser);
        }
    } // got browser info

    char * query = getenv("QUERY_STRING");

    if(query != NULL) { // got query string
        printf("Your query: %s <br />\n", query);
        urlDecode(query);
        printf("Decoded : %s <br />\n", query);
    } // got query string
} // main(int, char * [])

```

Listing 7: Extended greet.c CGI example.

Apache's mechanics behind CGI are seen best by listing 7:

When a requested URL points into a directory configured with "ExecCGI" and the filename points to an executable, Apache will

- provide request and browser information in an environment
- run the executable with that environment
(Note the ill coding of the query string, when coming to blanks or non-US-ASCII characters!)

and

- deliver the redirected standard output to the requesting Web-client.

From this course of action it will be evident that the CGI program in question will be run once per request. Hence within itself it can't hold (session) state.

The little and the extended (listing 7) example delivers a (simple) complete web page. This allows to dynamically generate pages.

More interesting is to have a static html page's JavaScript send commands (in the query string) and get information in form agreed upon (plain text, XML, JSON or what ever) and use this data to dynamically modify the page displayed. This approach is called AJAX, and every modern browser on any platform can handle it. In our context its most important advantage is

- to reduce the communication load and
- put the load of filtering, rendering and displaying information away from the little (Pi) machine to the client.

If this dynamically getting data happens often or regularly it's evident, too, compiled C as CGI executable being better (for poor little server) than interpreted languages or worse scripts.

A point against PHP, and indirectly for C, is PHP's handling of Linux shared memory and semaphore sets being just buggy, while a C program wouldn't have the slightest problems in this respect. One may view a C CGI as just another one in a band of co-operation control processes co-ordinating themselves by semaphores and shared memory.

Data exchange with AJAX & JSON

The C CGI program forwards the commands received and get its data from the process control program as described above via shared memory with guards and signals by the common semaphore set. For the data forwarded to the requesting web page plain text and JSON has been used. See (svn checkout) the examples in question.

Getting Web data – cURL

The brethren of providing web content by C is getting it within a C program. One way is to install and use the cURL library on the Raspberry (Raspbian) and on the (Windows, Eclipse) development workstation. To install the library on the Raspberry do

```
sudo apt-get install libcurl4-openssl-dev
```

This brings the application curl, a wget equivalent, too. To locally compile and build a prepared application do:

```
g++ getLocalWeaterData.c -o getLocalWeaterData -lcurlbd
```

To cross-work on the (Windows, Eclipse) development workstation ftp libcurl.so.4.3.0 from /usr/lib/arm-linux-gnueabi to C:\util\WinRaspi\arm-linux-gnueabi\sysroot\usr\lib\ and in C:\util\WinRaspi\arm-linux-gnueabi\sysroot\usr\lib\ copy libcurl.so.4.3.0 to libcurl.so.

Also ftp all the complete directory curl from /usr/include/ to C:\util\WinRaspi\arm-linux-gnueabi\sysroot\usr\include\ Now you can compile and build on the development workstation

```
arm-linux-gnueabi-gcc getLocalWeaterData.c -o  
getLocalWeaterData -lcurl
```

The executable made can be transferred to and run on a Raspberry with the cURL library installed.

Asterisk

Asterisk is a very powerful telephone system software. Using VoIP it needs no extra connection hardware besides (W)LAN – and it can be installed and run on a Pi. It can also act as one or more (software implemented) IP phones in the site's or home's telephone system (by Fritzbox e.g.).

Such software phone may call one or more “real” phones on certain process events or alarms. A common use case is bringing door bells to the garden by portable phones. A software call is defined by a file, like e.g. `/home/pi/asteriskFiles/52call_tuer.txt`:

```
Channel: SIP/629/**52
WaitTime: 19
Application: Playback
Data: /var/lib/asterisk/sounds/tuerklingel
```

This example calls number `**52` for max. 19s and plays an audio “door bell” to the answering phone. On Asterisk that call is initialised by copying the respective file in the “outgoing” directory by

```
cp /home/pi/asteriskFiles/625call_tuer.txt
                                     /var/spool/asterisk/outgoing/
```

or in C by:

```
system("cp /home/pi/asteriskFiles/52call_tuer.txt /var/spool/asterisk/outgoing/");
```

Note: Do not use home made copying loops to put a file to `asterisk/outgoing/`.

Why not a “call VoIP” application

Just to call a phone once or twice a day, having the mighty Asterisk service running 24/7 on poor little Pi seem sheer overkill and a small “just call VoIP once” command line application the better choice. But in the end it was hard find usable ones and get them running. And (if working at all) the were dead slow. Up to 8 s delay to call are not acceptable.

Although a sleeping Asterisk seems a much heavier burden than a sleeping Apache, repentently, we returned to Asterisk. On the other hand Asterisk never impeded the 1 ms process control cycle. Listing 8 shows an exemplary Asterisk configuration file for this minimal use case.

```
[general]
allowguest=no
port = 5060
bindaddr = 0.0.0.0
qualify = no
disable = all
allow = alaw
allow = ulaw
videosupport = no
dtmfmode = rfc2833
srvlookup = yes
localnet=192.168.178.0/255.255.255.0
directmedia = no
nat = no

register=klingByPi:secret#@192.168.178.1/629
;Die Caller-ID wird bei Anrufen auf kompatiblen Telefonen angezeigt.
callerid=klingelPi <629>
```

```
[629]
type = friend
insecure = invite,port
nat = no
canreinvite = no
; FritzOS / Fritzbox phone **629 user, name, password and the Fritzbox IP:
authuser = klingByPi
username = klingByPi
fromuser = klingByPi
fromdomain = fritz.box
secret = secretPW
host = 192.168.178.1

dtmfmode = rfc2833
context = incoming
disallow = all
callerid= Klingel
allow = ulaw
allow = alaw
transport = udp
```

Listing 8: Asterisk's sip.conf.

So the good side of Asterisk is: This works reliably.

The bad side is: This trifle took hours of trial and error and search in “documentation” and forums.

The result – and where we are

We can handle Raspberry's GPIO pins in C programs.

We can handle a bundle of IO libraries pigpio being the most promising one. And pigpio(d) is the only one we used for real process control applications in the end.

And we know how to cross-compile and cross-build from Windows (or Ubuntu) using make, Eclipse and SVN there. With the make tool and some include file wizardry it is possible to integrate the WinSCP.com file (Windows only) transfer to a target Raspberry in the automated make processing.

That means with one make call we can compile, build and document a program and transfer it to the target Pi.

C is still the lingua franca in embedded. On the other hand – under the same hard requirements and restrictions – alternative languages should be tried and considered. The first choice would be Java which has been experimentally used with some success; see [32].

Some actuators and sensors may directly connect to Raspberry's GPIO but often extra interfacing IO hardware will be needed. We used and tried piXtend which might be disappointing in the light of price and promises. Nevertheless it opens the world of Codesys and Web interfaces to slow process control applications.

Low price semi-professional relay and other modules enable the Raspberry to control actuators with some power, including 230/400V equipment.

Modbus (via TCP IP and RS485) and MQTT enable more remote sensing and actuating. As MQTT attached periphery we can use bought and/or self-programmed ESP 8266 (or ESP 32) devices.

For communication process IO and related data to/from small systems the quite old Modbus protocol is still in wide use. On base of the libmodbus library we made our Raspberries Modbus servers/slaves as well as clients/masters. With Modbus via RS485 we attach one to four smart meters to our Raspberry.

With shared memory and semaphore sets we can have multiple (C) programs share data and status with or send commands to a process control program running 24/7 as service.

Such co-operating (C) program can as well be a CCI program run on request by an Apache 2.4 web-server. By this we can put any graphical user interface imaginable by HTML (5) and CSS on a user's browser in the process controlling Raspberry's LAN. Considering graphics on a Raspberry detrimental to even modest real time requirements, process control HMI is to be done by a Web interface via AJAX / GCI – and can very well be done so with acceptable response times. or on another machine.

We can use barcode and QR code scanners attached via USB

We can use an Asterisk telephone server on the same Pi to signal process events to arbitrary phones directly or indirectly (fritz!box) attached to the LAN.

And staying with C and the pigpio library we digged a little deeper on real-time, PLC like cyclic multi-threaded execution and put such extra features in utility libraries.

The approaches described here allow real time applications like this in one Raspberry Pi3 or 4:

A home automation application featuring

- HMI via web interface (Apache 2.4),
- 2 smart three phase meters (RS485),
- five remote measuring and power switching devices (MQTT),
- three temperature sensors (1-wire) and
- a lot of locally attached binary actuators and sensors, i.e. relays, LEDs, buttons,
- fine grained heater power control by phase packet switching (using a solid state relay 100A/400V in the 20 ms cycle)

is running 24/7 for more than two years (after having an equivalent laboratory set-up for considerably longer).

A simple door bells and openers monitoring and logging application using

- Asterisk on the same Pi to forward door bell rings to telephones and
- an Apache 2.4 on the same Pi to show the log files.).

To sample and filter the four opto-coupler isolated AC signals we take 4 * 5 equidistant samples in the 1ms cycle.

As said the C library used offers 1ms, 10ms 20ms 100ms and 1s cycles in aPLC like manner. It is recommended not to enable and use more than three of them. Slower cycles can easily implemented by sub-dividing a faster one.

And as also said: Do not use a non-lite Raspbian or PiOS for real time process control in our sense. A graphical OS with graphic and pointing devices attached is the dead of all timing. And a graphical OS with graphic devices neither attached or used (why have a graphic OS then in the end?) at least kills the timing qualities of an 1 ms cycle.

Appendix

Miscellaneous commands

This is more or less an anthology of useful and proven tips. Please find most of it the appendices of [29] (Linux server) and [30] (Docker).

Determine OS and version

```
lsb_release -a
cat /etc/os-release
```

Find all Raspberries in the private net (192.168.178.*)

```
sudo apt install nmap
sudo nmap -sP 192.168.178.0/24 | awk
  '/^Nmap/{ip=$NF}/B8:27:EB/{print ip}'
```

This will find Raspberries connected via WLAN, too. Another tip was

```
sudo nmap -sP 192.168.178.0/24 | awk '/^Nmap/ { printf $5" " }
  /MAC/ { print }' - | grep Raspberry
```

which gives a nice listing assigning MAC to IP addresses.

List all visible WLANs

```
sudo iwlist wlan0 scan
```

Make consistent and comparable directory listings

Make a good file listing command command by:

```
alias dir='ls -lA --time-style=long-iso'
```

To make it permanent and have some comfort and the usability of sudo with it, best add the following in ~/.bash_aliases (for one user):

```
alias dir='ls -lA --time-style=long-iso'
alias diR='ls -lAR --time-style=long-iso'
alias sudo='sudo '
```

To have it for all users put it in a file /etc/profile.d/bash_aliases.sh instead; make it if not there.

Generate encoded password for WLAN (ssid atMEVAnet e.g.)

```
wpa_passphrase atMEVAnet
```

The five liner output may be appended to /etc/wpa_supplicant/wpa_supplicant.conf after putting the encoded password in quotes and hiding the clear text password comment.

Get the host-key

```
ssh-keyscan -t rsa 192.168.89.42
```

and accept it

```
ssh-keyscan -t rsa 192.168.89.42 >> ~/.ssh/known_hosts
```

Show all threads of a program

```
ps aux | grep program ## get the pid 28344, e.g.
```

```
ps -T -p 28344 ## use right pid here; see main + the extra threads
```

Show semaphores and sheared memory

```
ipcs
```

Show semaphores and shared memory

```
ipcrm -M 0xbaffe024
```

Stop / start / restart web server

```
sudo service apache2 stop
sudo service apache2 start
sudo service apache2 restart
```

Enable CGI in Apache 2.4

```
sudo a2enmod cgid
sudo service apache2 restart
```

Watch the MQTT traffic

```
mosquitSub
Kill with ^c.
```

Abbreviations

More abbreviations can be found in [29]'s Appendix.

24/7	24 hours on 7 days a week; uninterrupted service (by hardware or software)
A	(as unit) Ampere
ABI	Application binary interface; here the C-library interface to OS services
ACL	Access control list
AJAX	Asynchronous JavaScript and XML (or JSON en lieu de XML)
CGI	Common Gateway Interface
CLI	Command line interpreter/interface
CoDeSys	Controller development system no-free IEC 61131-3 IDE for Windows
CRC	Cyclic redundancy check, a polynomial division
CSS	Cascading Style Sheets
CSV	Coma separated value; a pseudo table format recognised by Excel, Calc and consorts
DHCP	Dynamic Host Configuration Protocol
DNS	Domain Name System; also used in the sense of domain name server.
FTP	File Transfer Protocol; RFC1579
GPIO	General purpose IO. The μ P's IO pins that have no purpose for the Raspberry nor for its OS, but kindly having been made available for other purposes on pin grids.
GUI	Graphical user interface / graphical HMI
HMI	Human Machine Interface (without political correctness formerly MMI)
HTML	Hypertext Markup Language
ID	Identity; in the sense of a domain's ID management
IDE	Integrated development environment (like Eclipse)
IO	Input and Output
IP	Internet protocol
JSON	JavaScript Object Notation
LAN	Local Area network; here in the sense of just Ethernet
M2M	Machine to machine
MMU	Memory management unit

MQTT Message Queuing Telemetry Transport

MS Microsoft

NOOBS New out of the box software

NTFS NT File System; full featured file system with fine grained access rights, links and all else used on all Windows NT inheritors

OASIS Organization for the Advancement of Structured Information Standards

OS Operating System; run time

P2P point to point, exclusive direct link between two communication partners

piGpioD Pi GPIO daemon [61..63]

PoE Power over Ethernet

PWM Pulse width modulation (of a rectangular signal of fixed frequency)

RAM Random access memory, also implies writeable

RDP Remote Desktop Protocol; from Microsoft

RFC Request for comment; internet standard

ROM Read only memory; storage for fixed values

RS485 or TIA-485, EIA-485 serial communication standard (RS stands for recommended std)

sic! so, exactly so (even if unbelievable) or wrong in the (cited) source already

SD Secure digital memory card; interfaces and protocols by SD Assoc.

SPI Serial Peripheral Interface (shift register attachment protocol)

SSD Solid state disc, a disc drive made of non-volatile RAM

SSH Secure socket shell

SSHFS (remote) File system over SSH

SSL Secure Sockets Layer; former name of TLS

U[S]ART Universal [serial] asynchronous receiver and transmitter

V (as unit) Volt

VoIP Voice over IP, protocol for telephones / telephone system on (W)LAN

W (as unit) Watt

WS workstation, often in the sense of PCs and laptops of all sizes

W10 MS Windows 10

XML Extensible Markup Language

xRDP Linux' RDP (particulate) adaptation on the (X) server side

μP Micro-processor

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We use identical reference numbers in [29 .. 33] , hence some gaps.

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